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B	<div>ROMEO DISCRETE SI BUILD</div> <div>2009/11/13</div>																																																																																			
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**INVENTEC**

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CHANGE by *Evan Shih* 13-Nov-2009

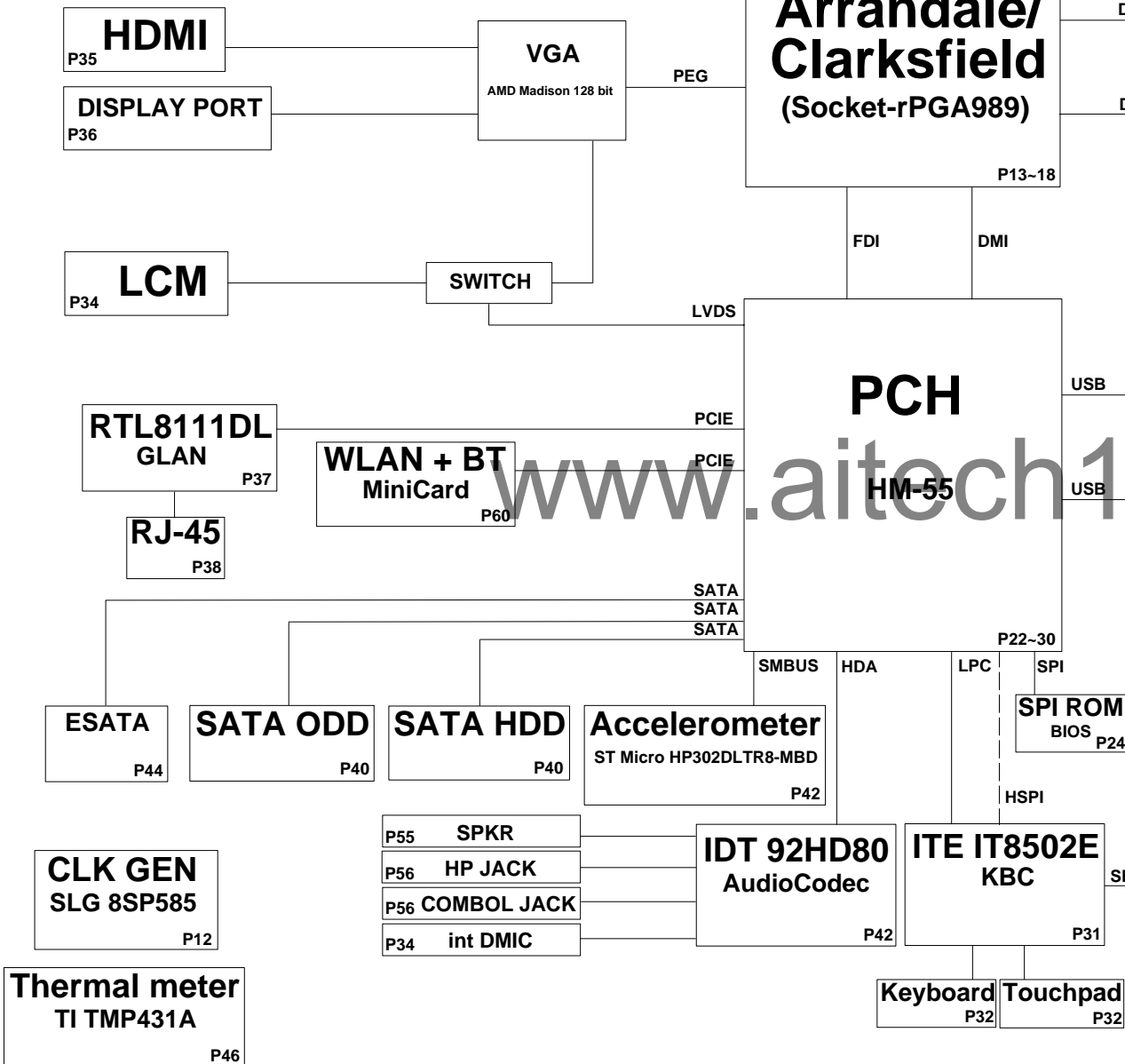
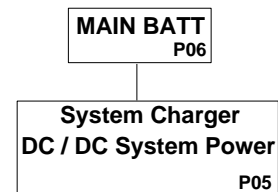
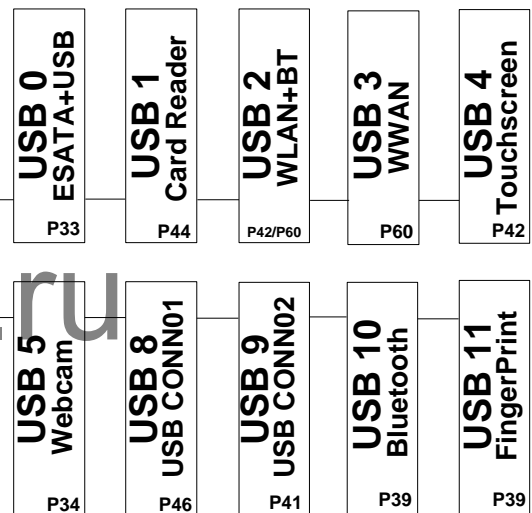
Inventec Confidential

Arrandale/  
Clarksfield  
(Socket-rPGA989)

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800/1066/1333 MHz , 8GB maximum memory P20

DDR3 SO-DIMM 2  
800/1066/1333 MHz , 8GB maximum memory P21

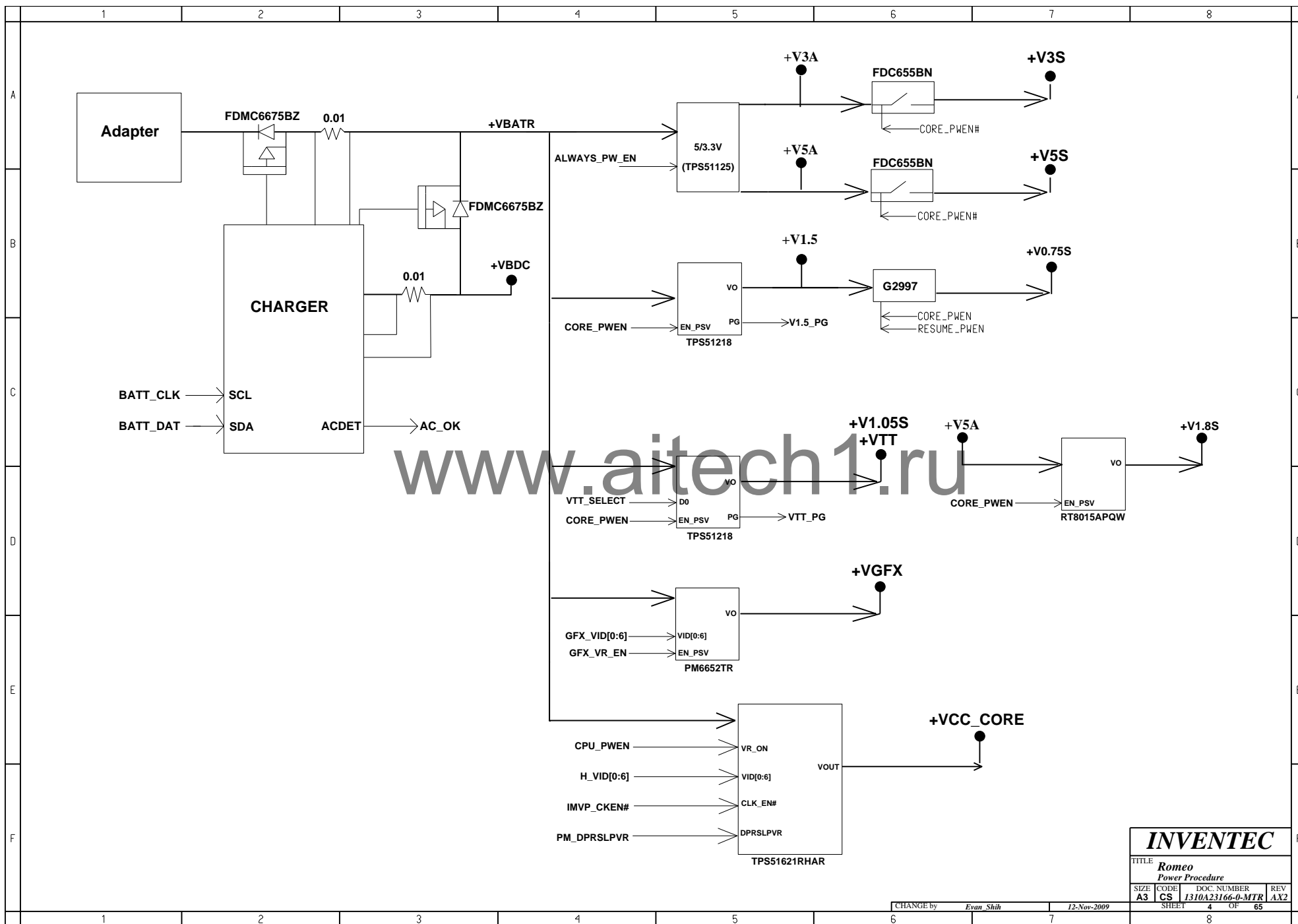
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INVENTEC

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A3	CS	1310A23166-0-MTR	AX2
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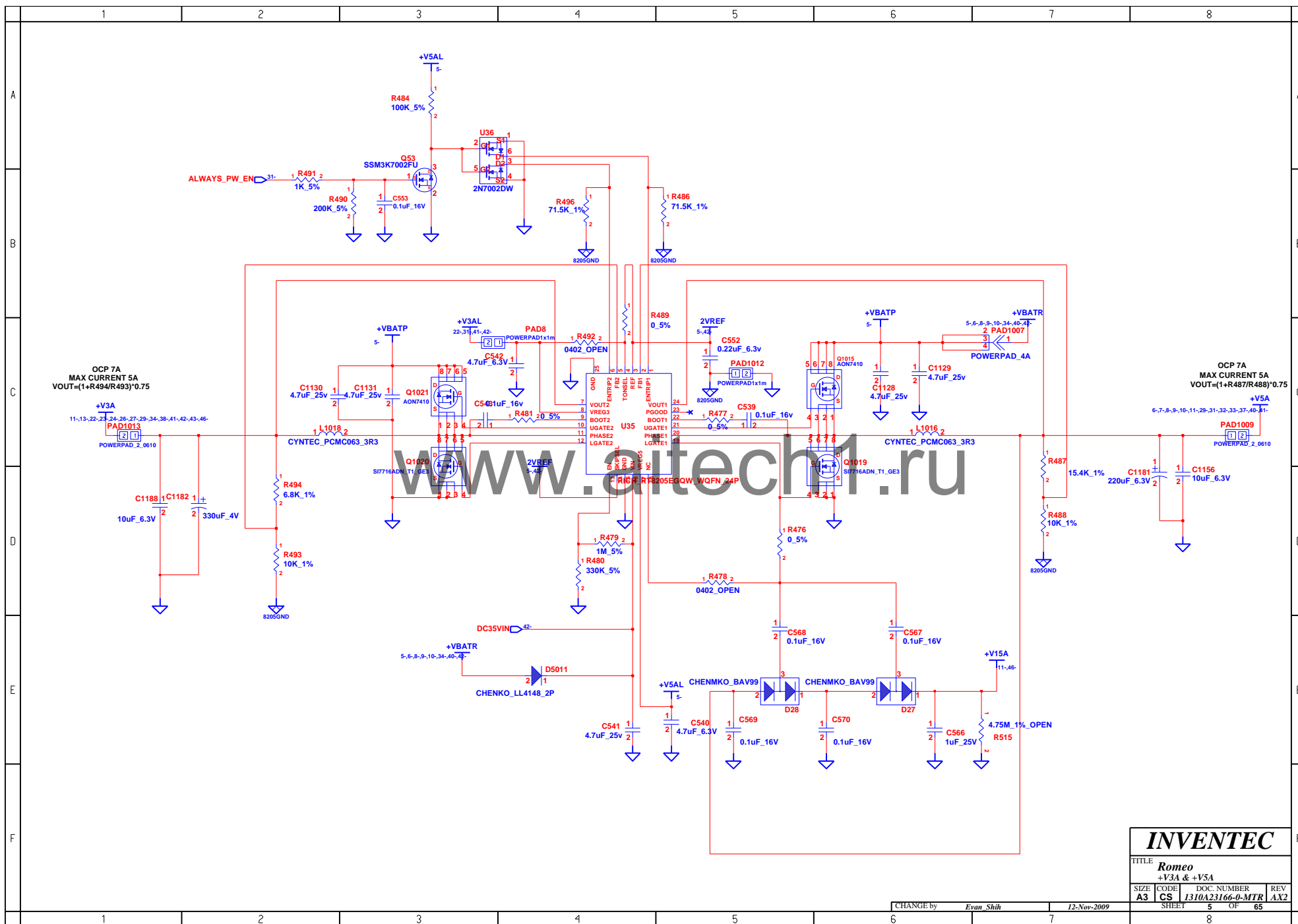
CHANGE by Evan Shih 13-Nov-2009



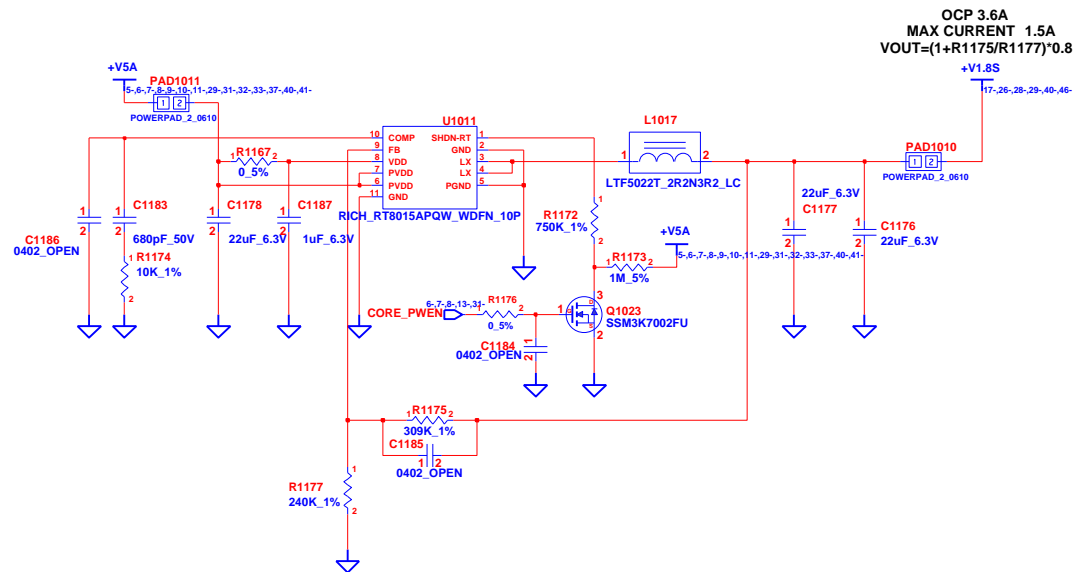
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Romeo			
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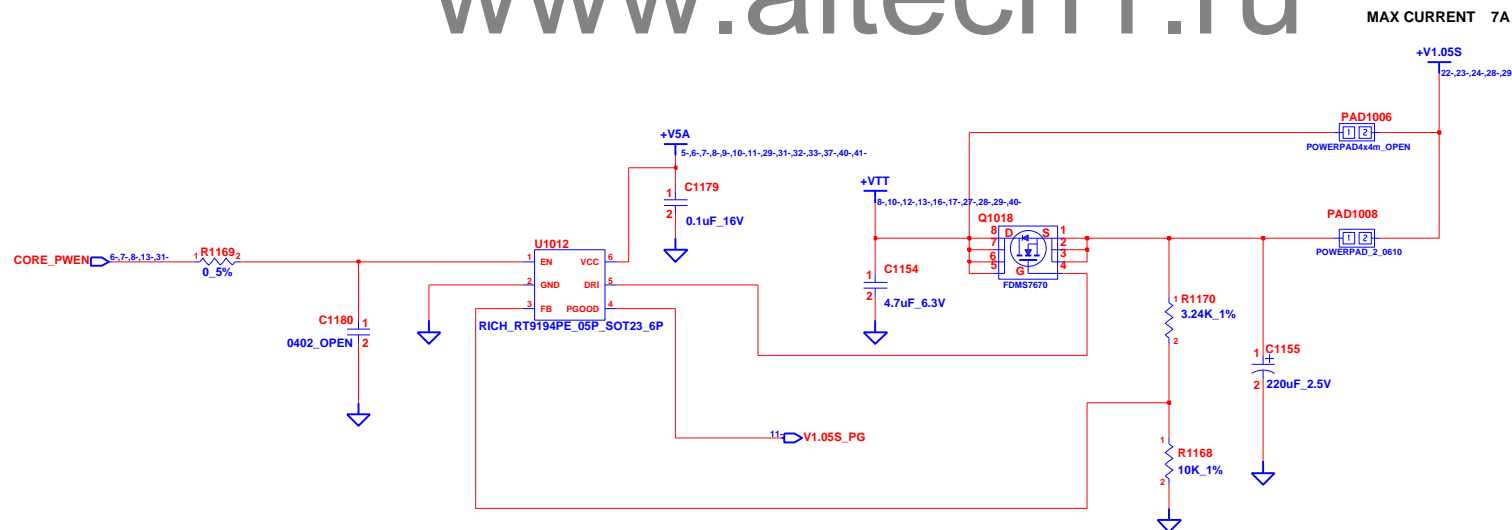
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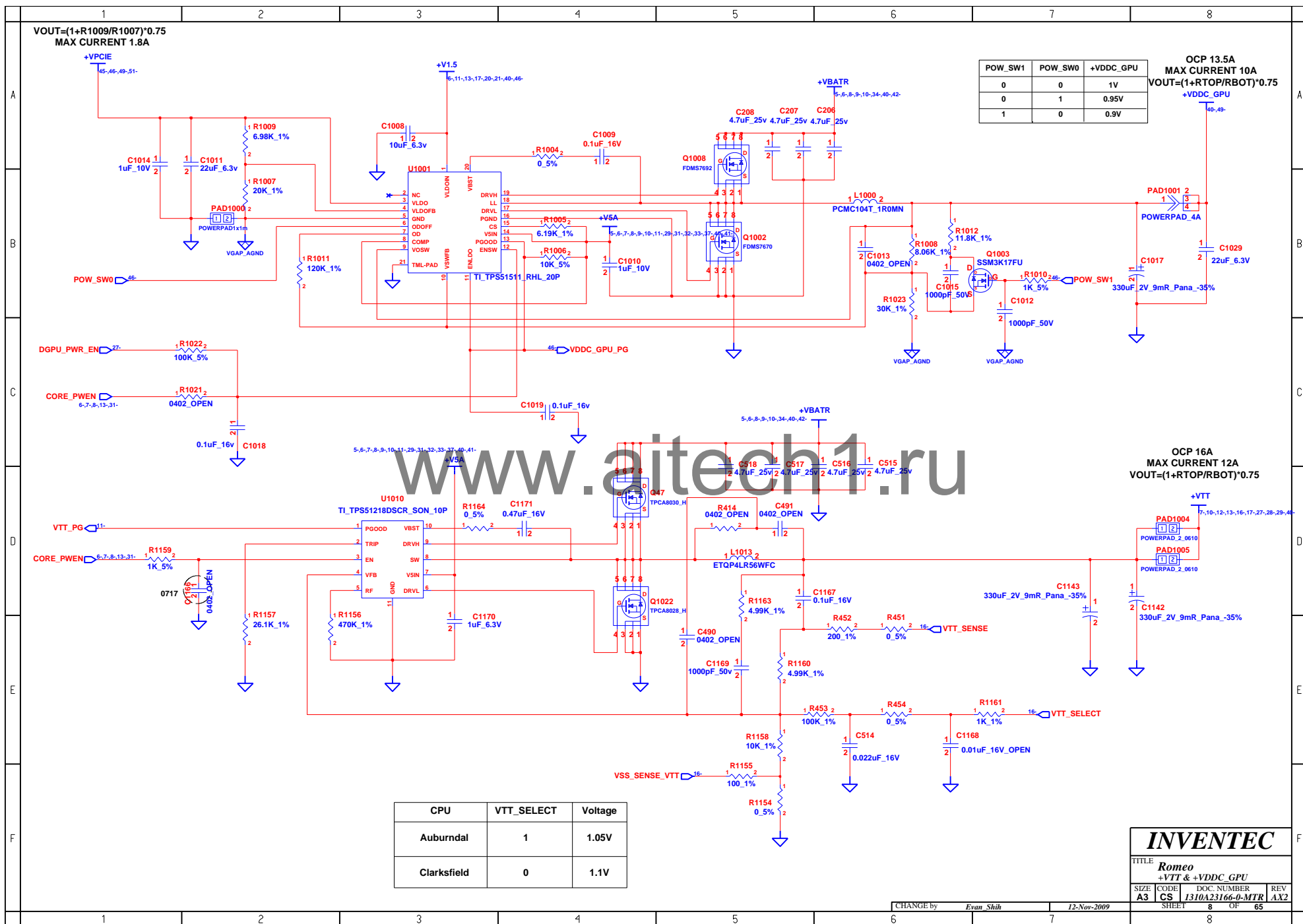
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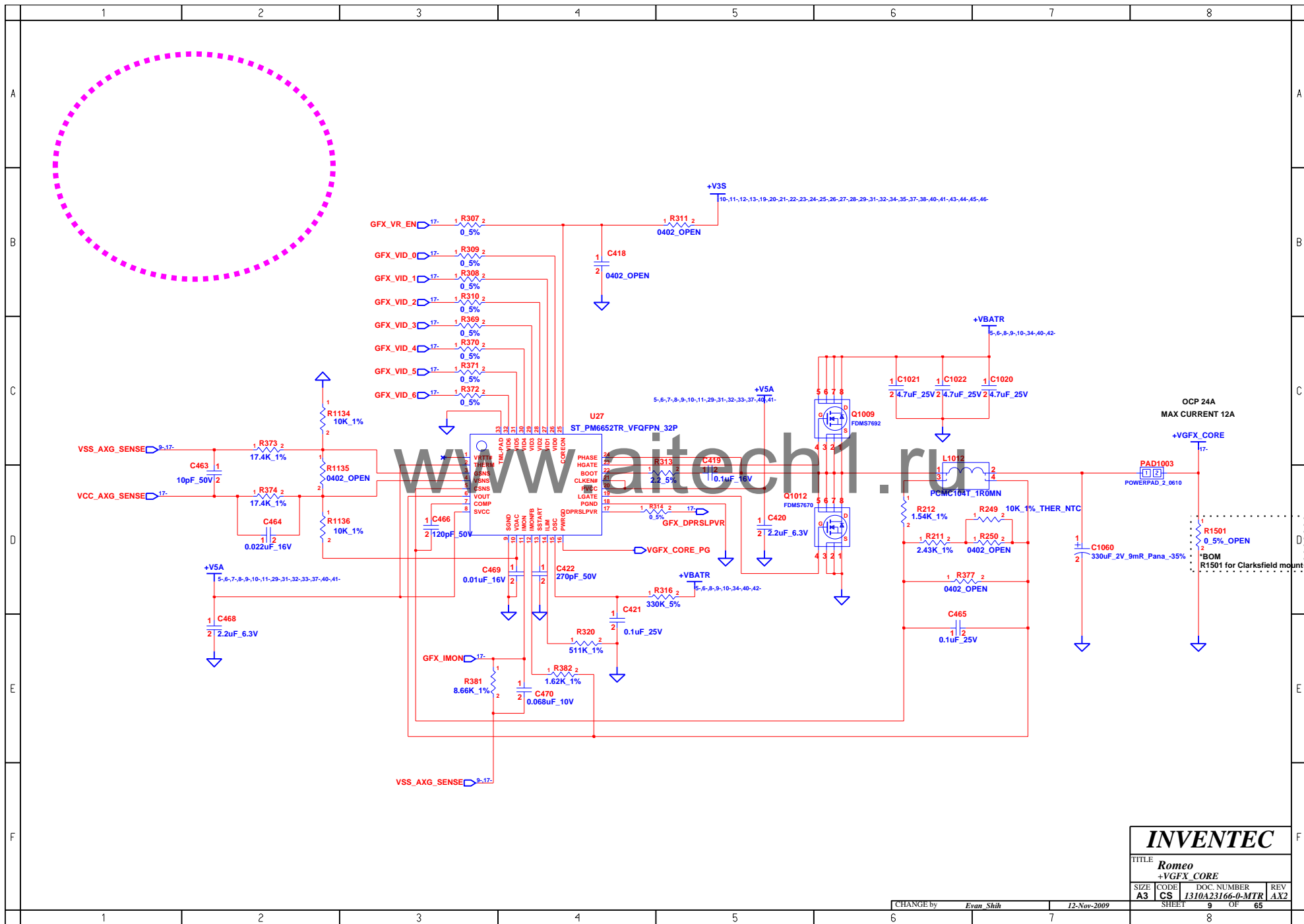
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Romeo			
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CHANGE by Evan Shih 12-Nov-2009





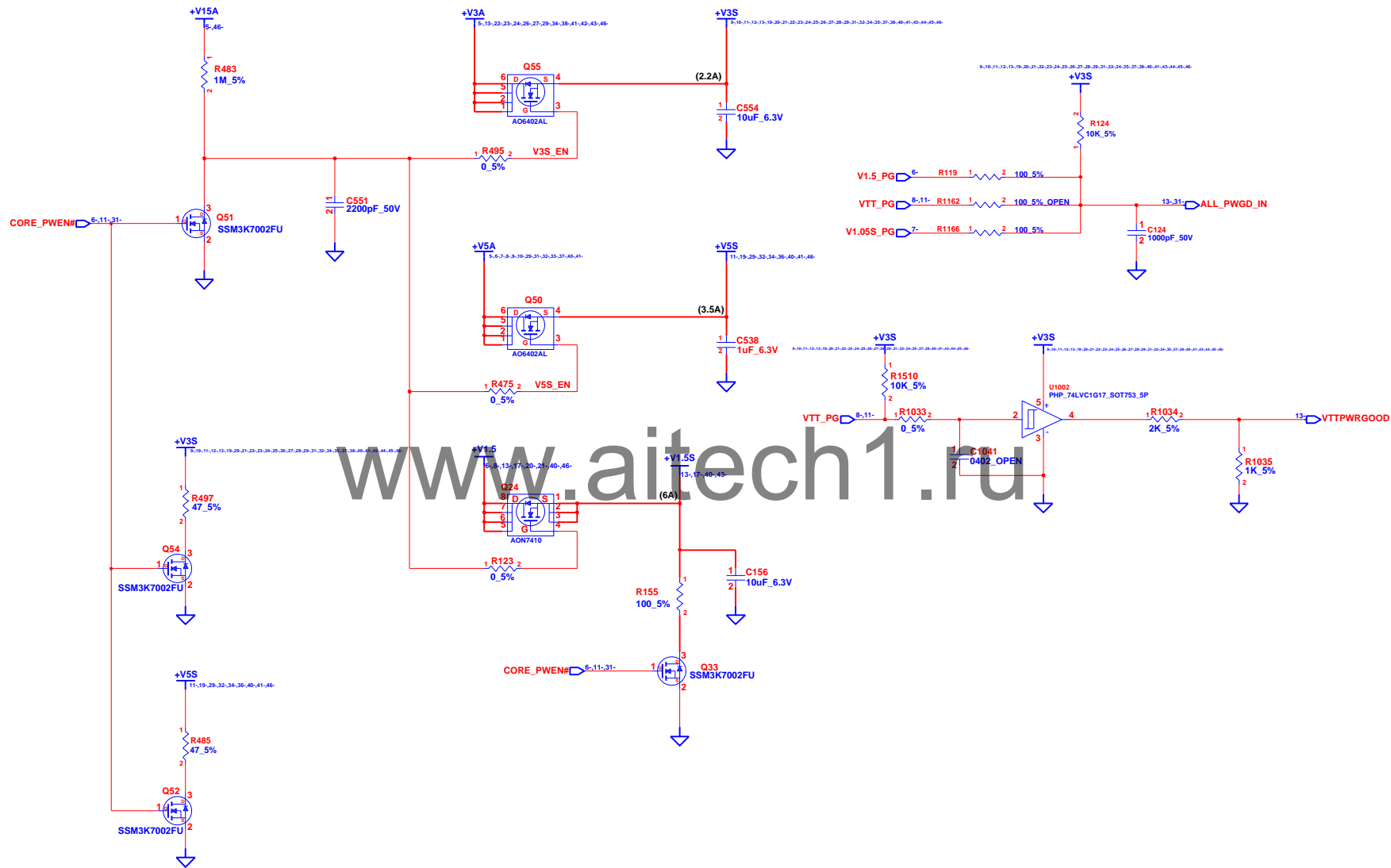


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A3	CS	1310A23166-0-MTR	AX2
SHEET		9	OF 65

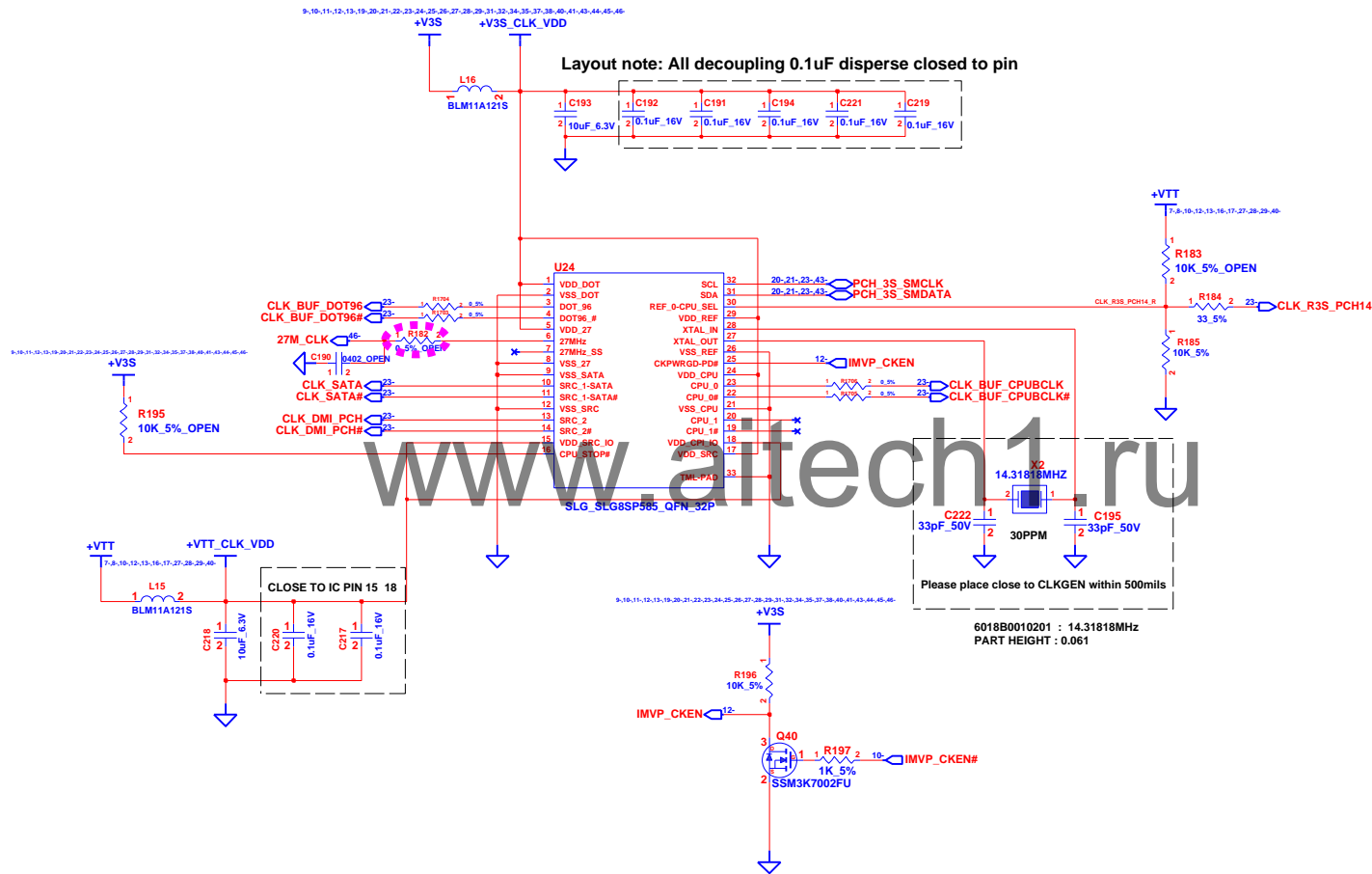
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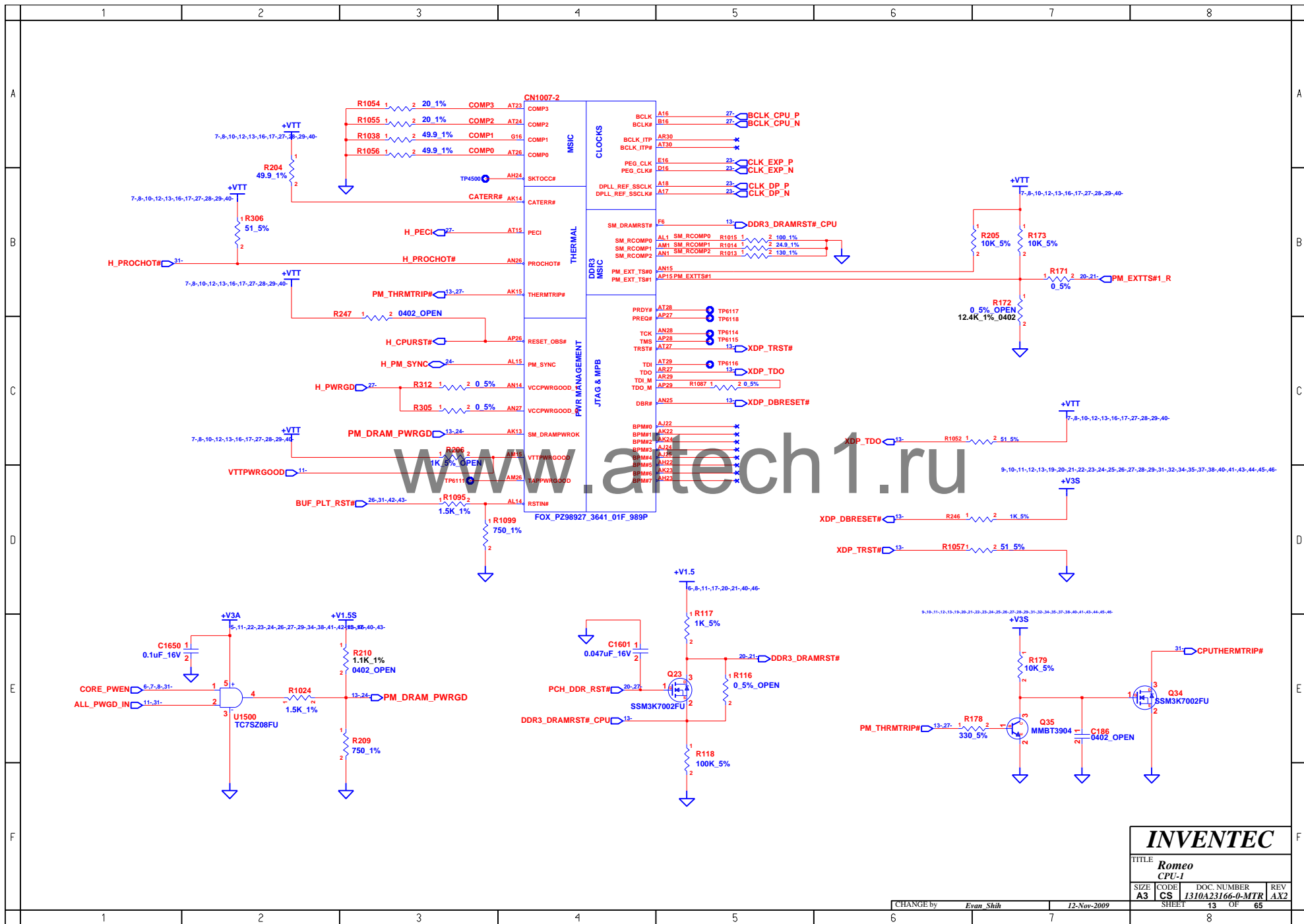
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CHANGE by Evan Shih		12-Nov-2009	
SHEET 11		OF 65	



**INVENTEC**

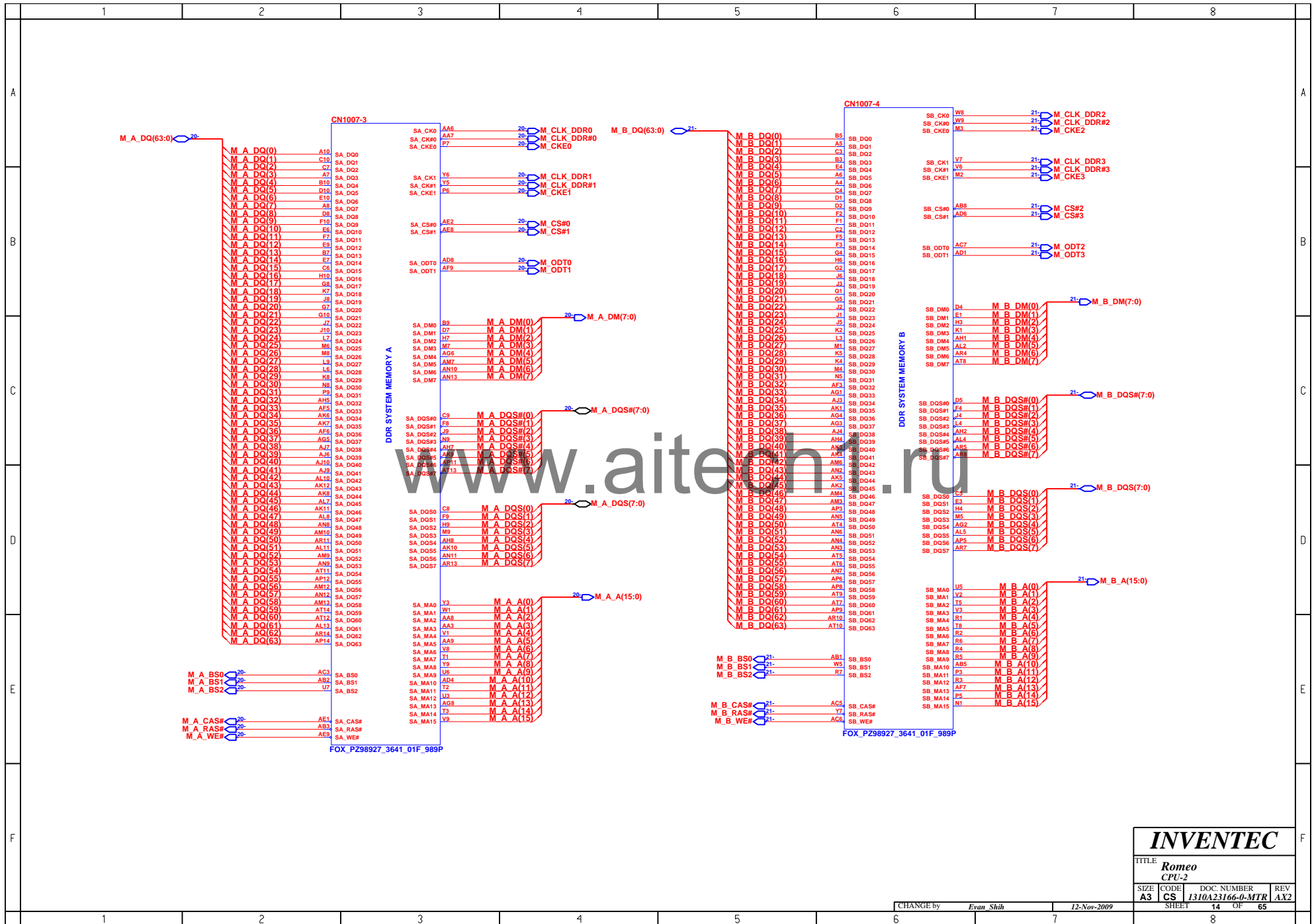
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SHEET <b>12</b>		OF <b>65</b>	

CHANGE by *Evan Shih* 12-Nov-2009

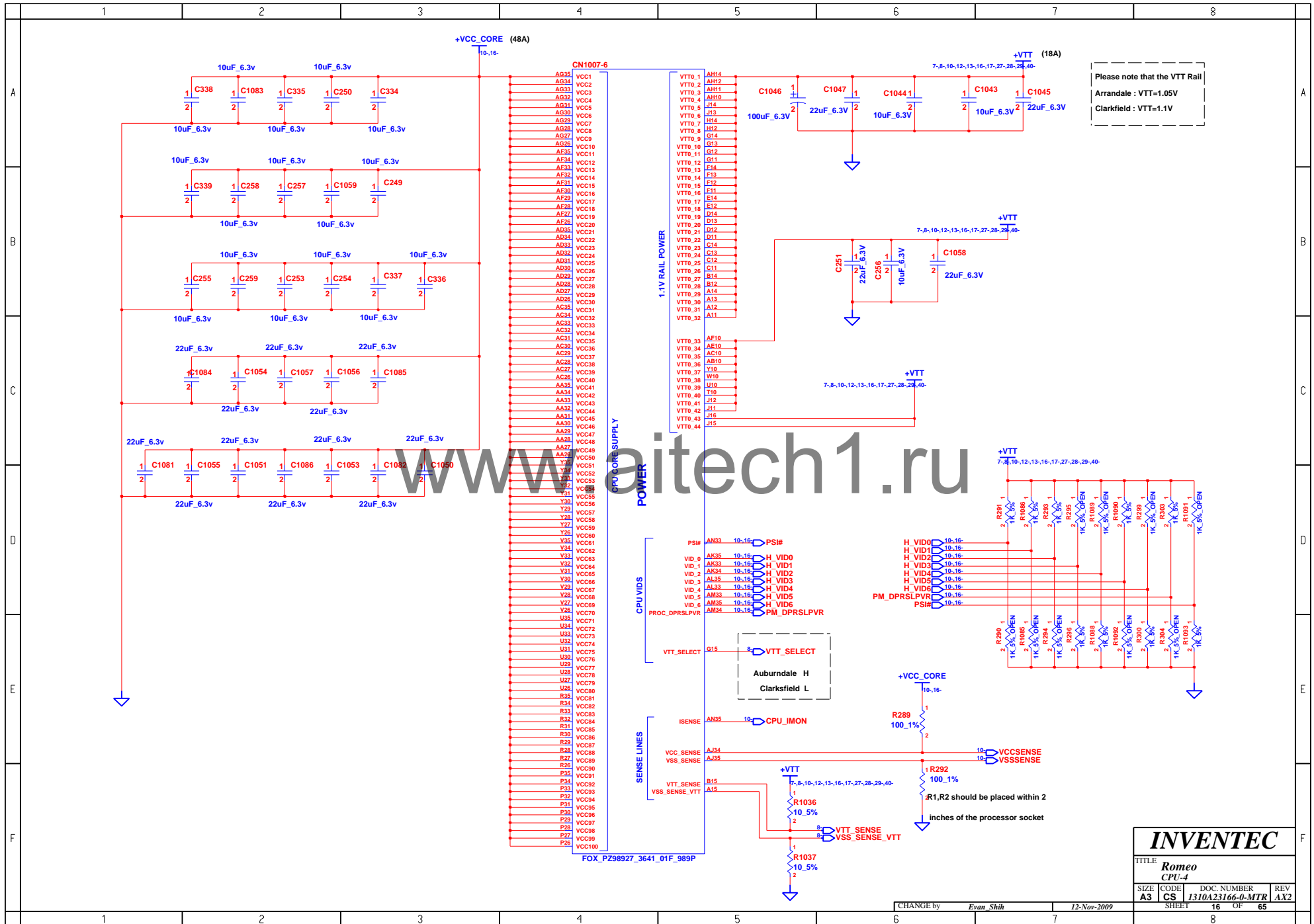


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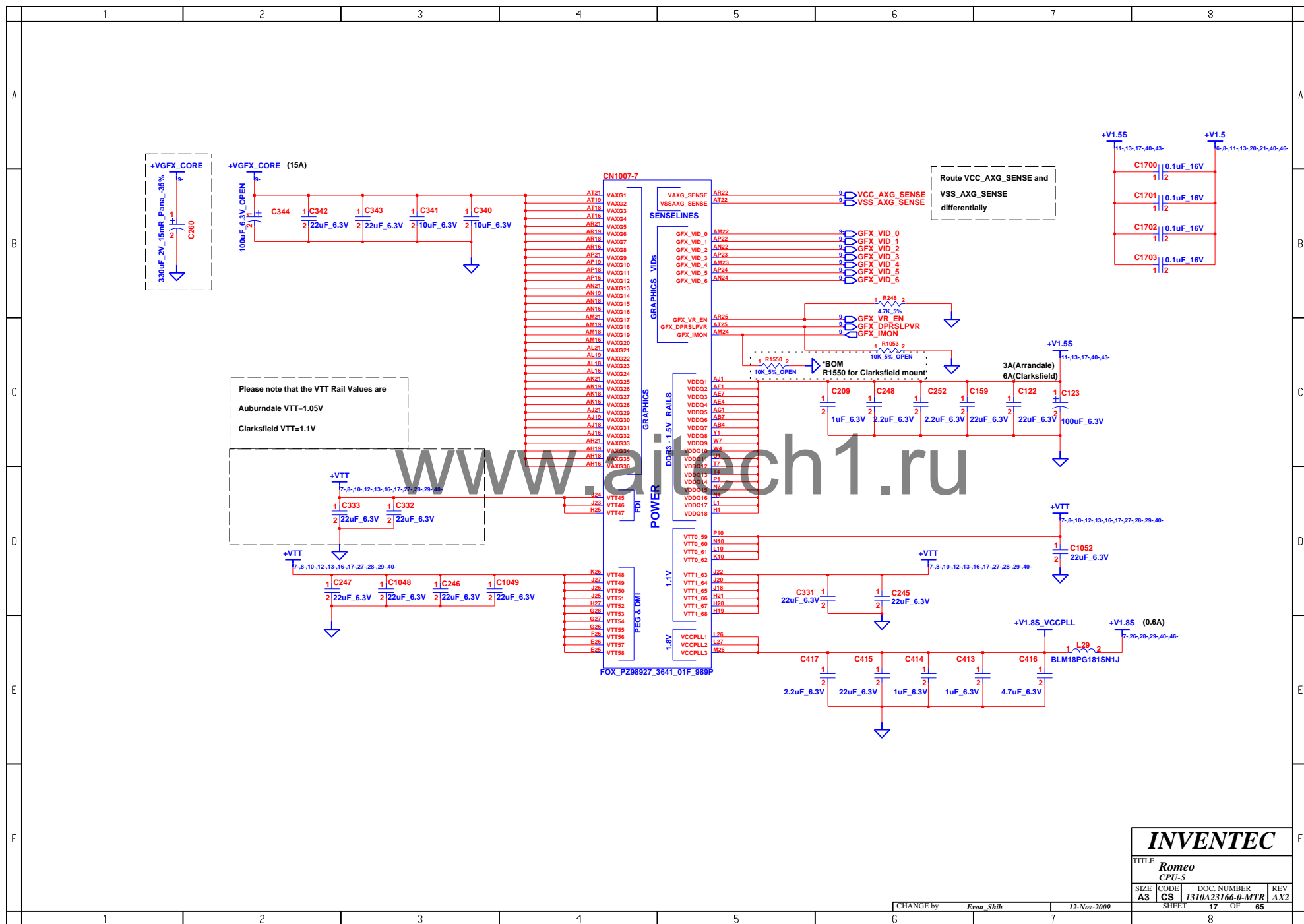
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CHANGE by		12-Nov-2009	
Evan Shih			
SHEET		13	OF 65

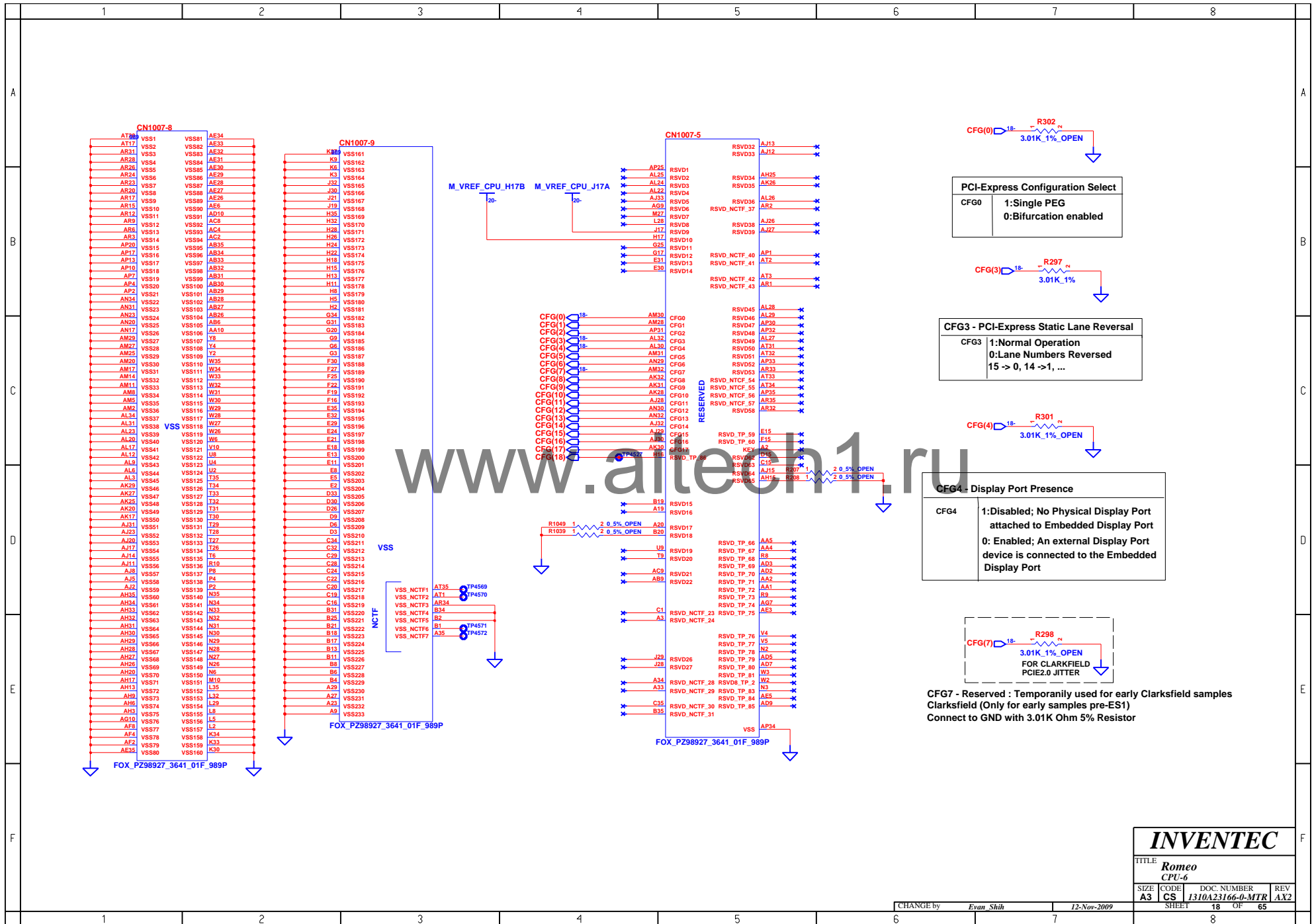








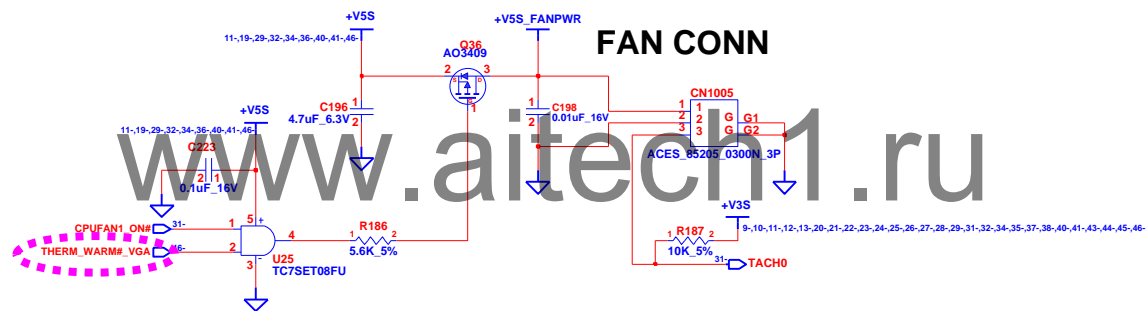




**INVENTEC**

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SHEET 18		OF 65	

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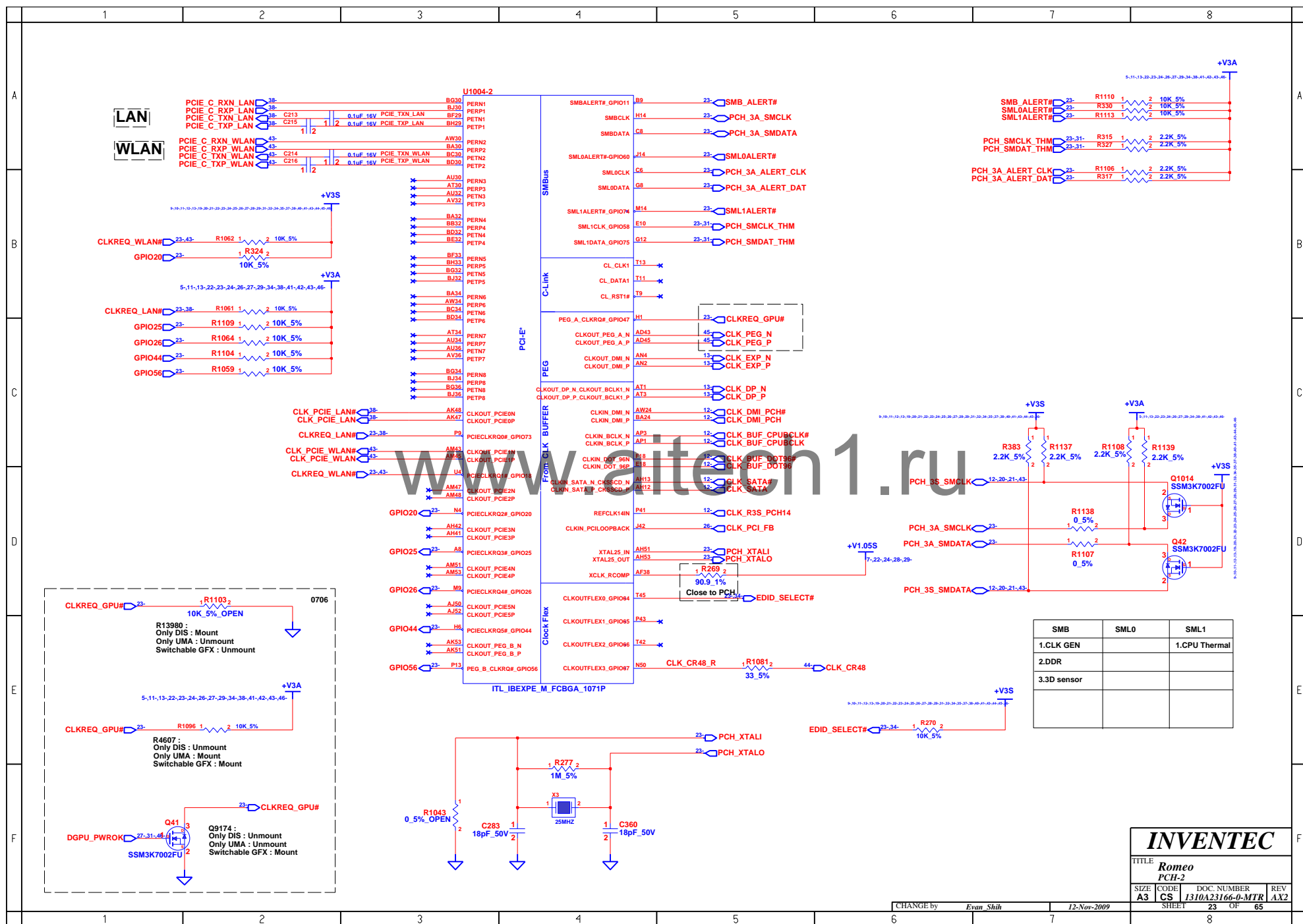


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CHANGE by <i>Evan Shih</i>		12-Nov-2009	
SHEET		19 OF 65	







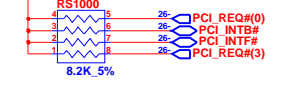
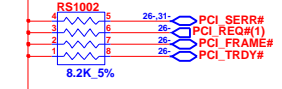
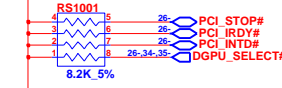
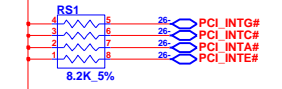
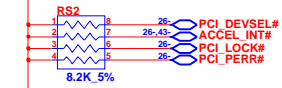








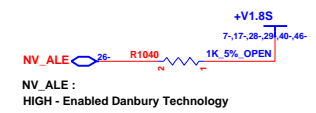
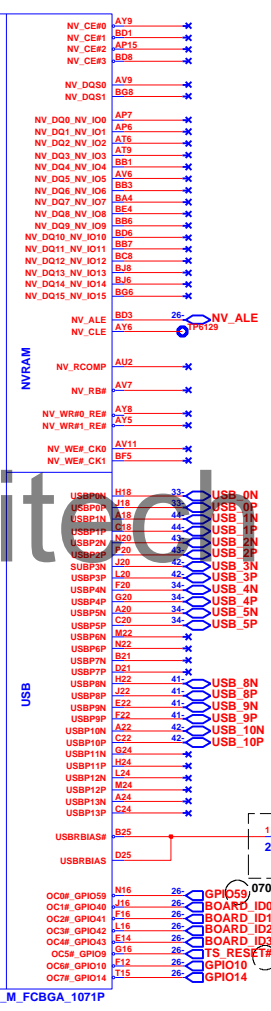
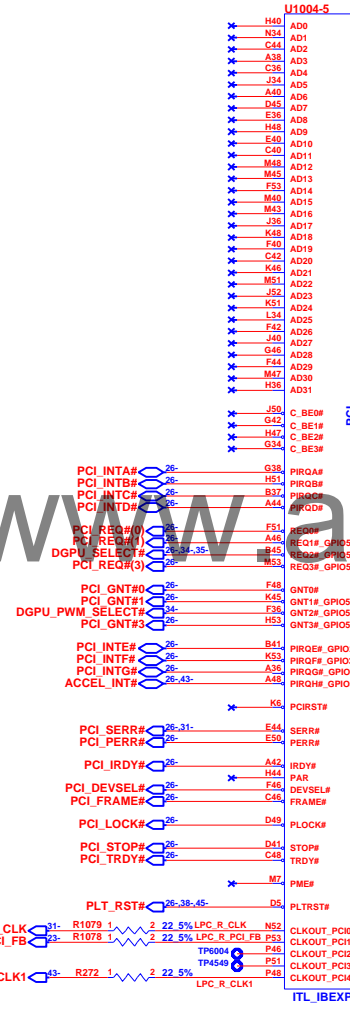
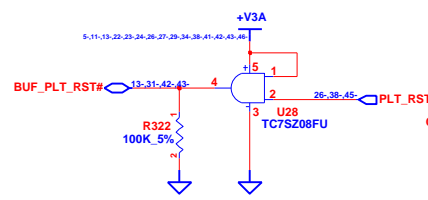
+V3S  
5, 10, 11, 12, 13, 14, 15, 16, 20, 21, 22, 23, 24, 25, 27, 28, 29, 31, 32, 34, 35, 37, 38, 40, 41, 42, 43, 45, 46



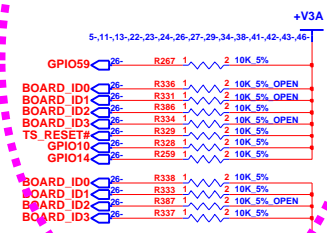
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0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI



PCI\_GNT#3 :  
LOW - A16 swap override / Top-Block Swap Override enabled



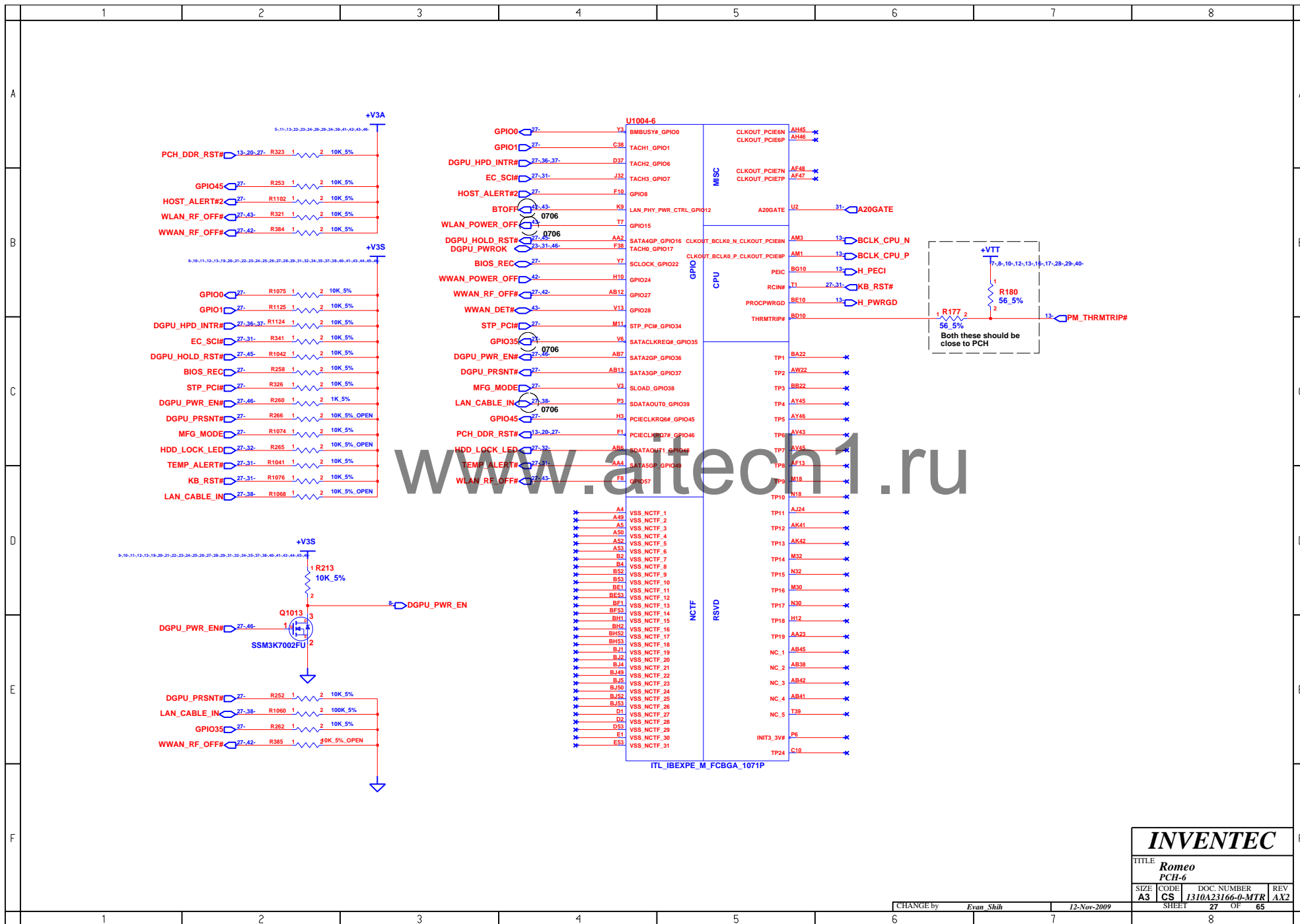
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AD	0	1	0	0
CK	0	1	0	1



INVENTEC

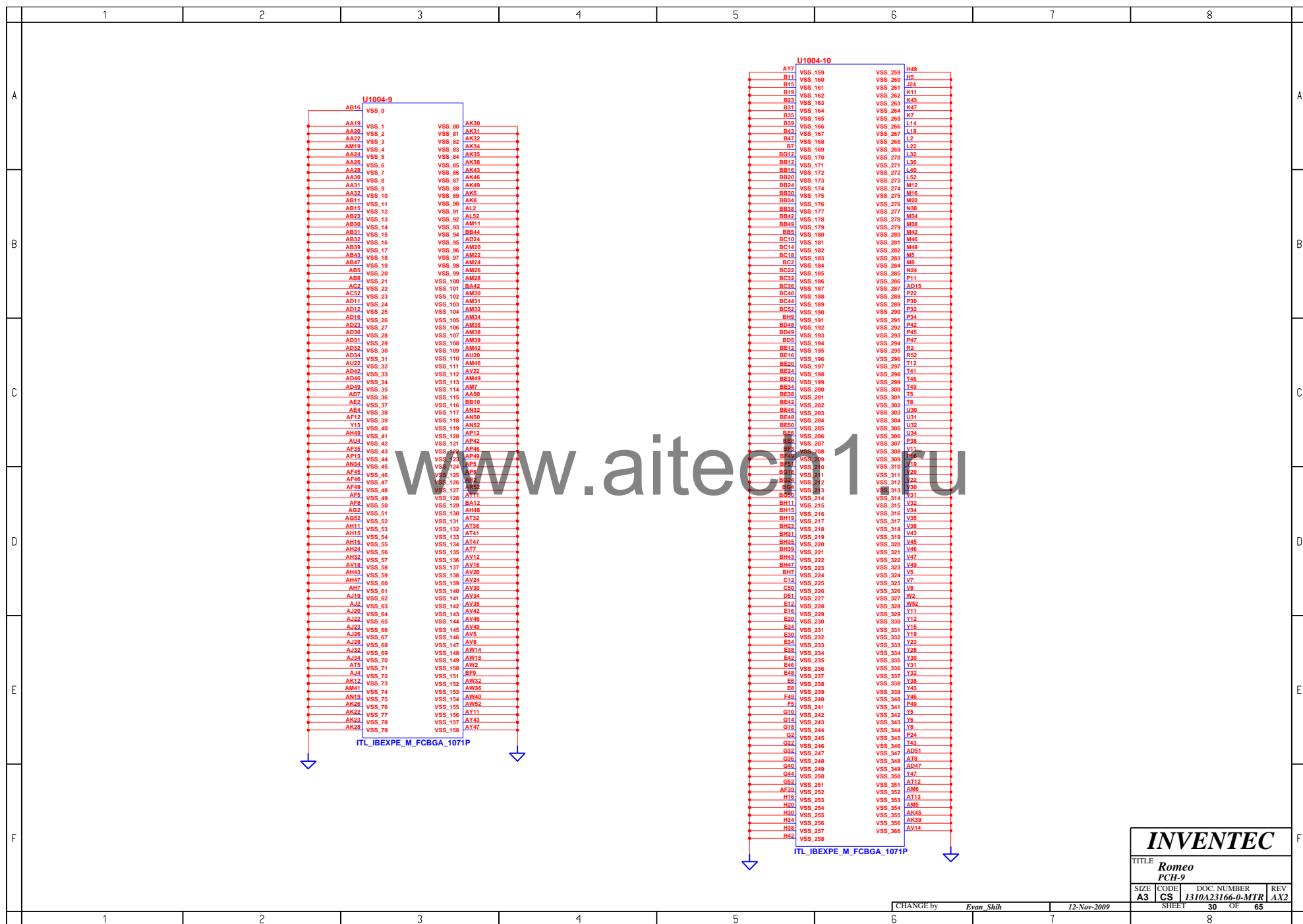
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SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV AX2
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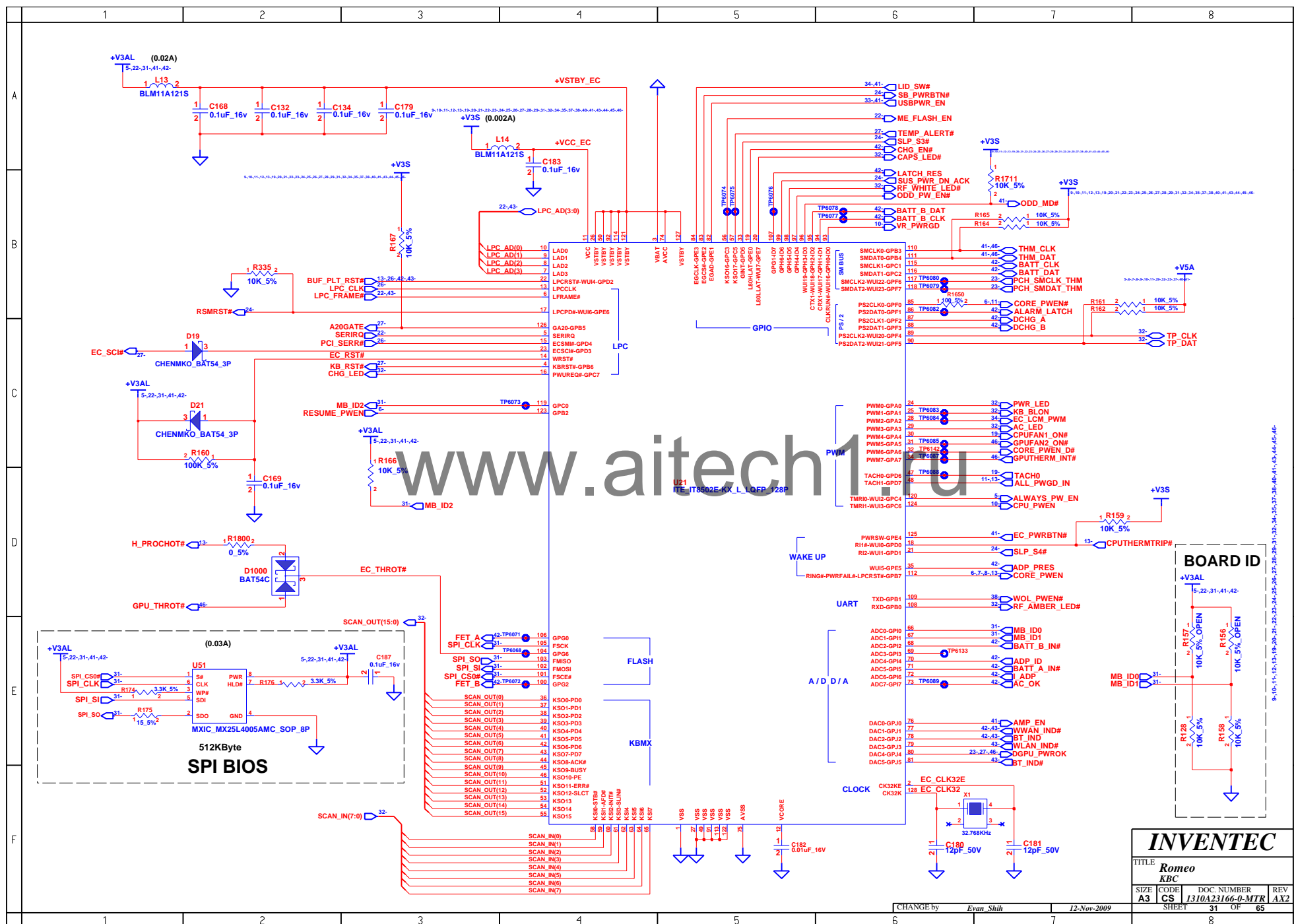
CHANGE by Evan Shih 12-Nov-2009

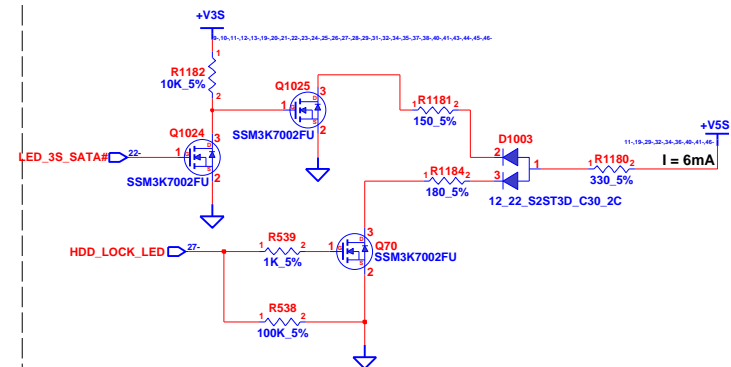
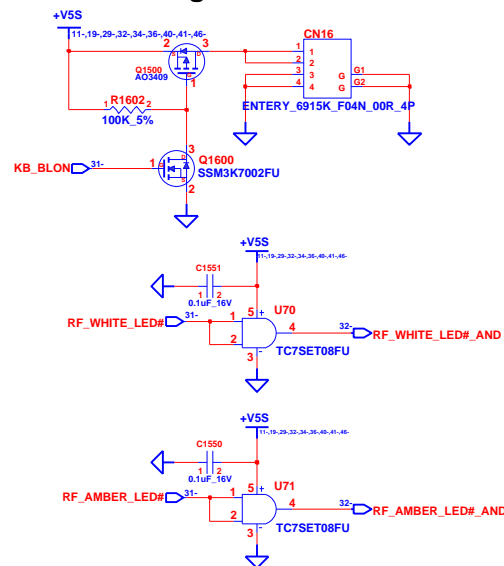
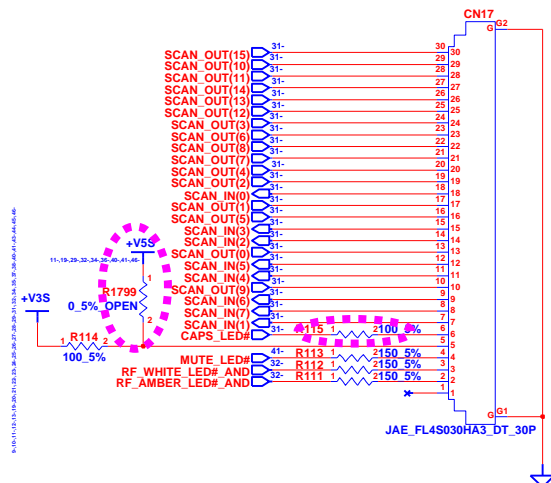
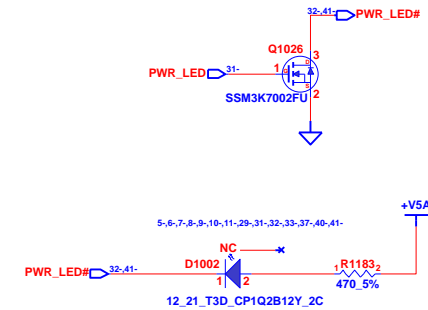
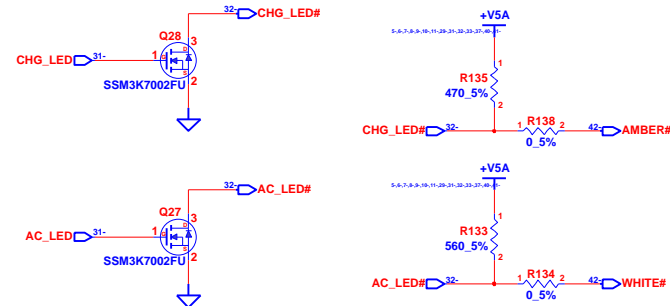
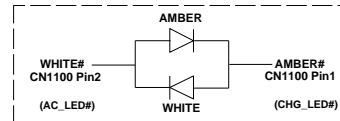
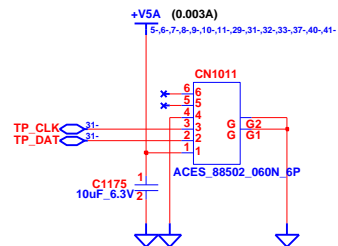








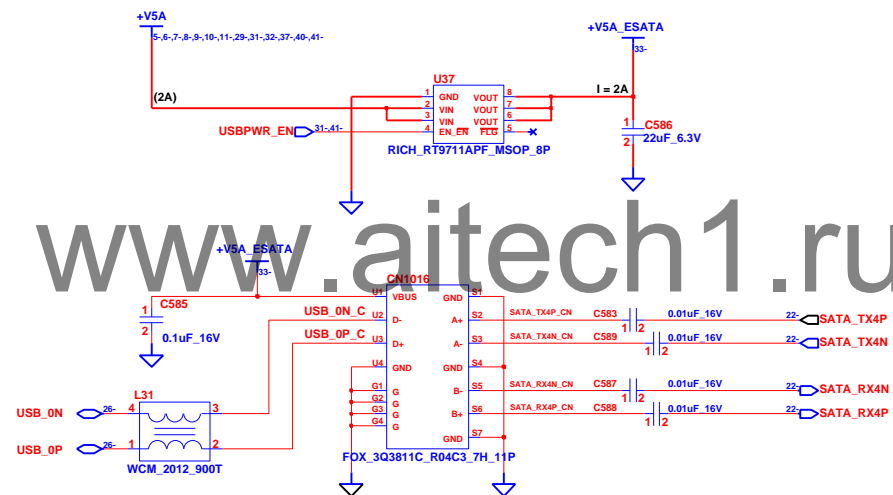




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<b><i>Romeo</i></b> <b><i>Keyboard &amp; Touchpad</i></b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23166-0-MTR</b>	<b>AX2</b>
SHEET		32	OF 65



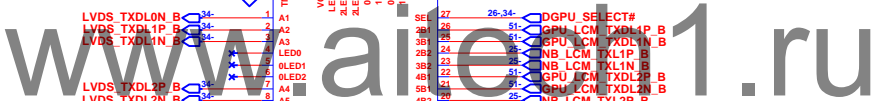
# ESATA CONN (without Re-Driver chip)



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SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>AX2</b>
CHANGE by <i>Evan Shih</i>		12-Nov-2009	
SHEET		33	OF 65



TITLE			
<b>Romeo</b> <b>LCM</b>			
SIZE	CODE	DOC. NUMBER	REV
<b>A3</b>	<b>CS</b>	<b>1310A23166-0-MTR</b>	<b>AX2</b>
SHEET		34	OF 65



DGPU_SELECT#	OUTPUT
L	MADISON
H	PCH

**INVENTEC**

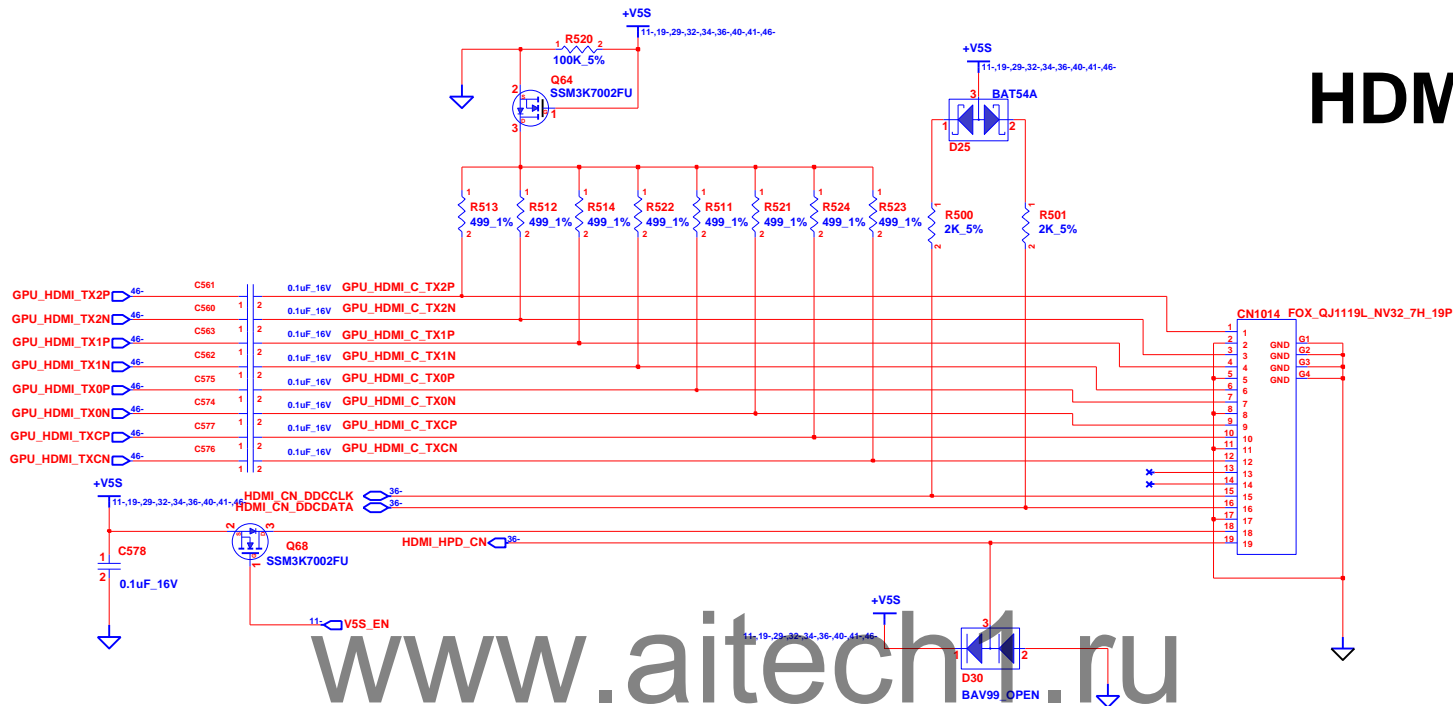
TITLE	<i>Romeo</i>
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SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>AX2</b>
SHEET		35	OF 65

CHANGE by	<i>Evan Shih</i>	<i>12-Nov-2009</i>
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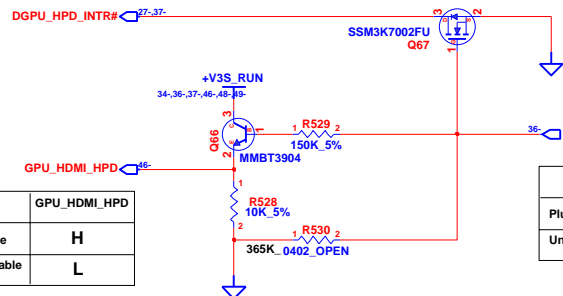
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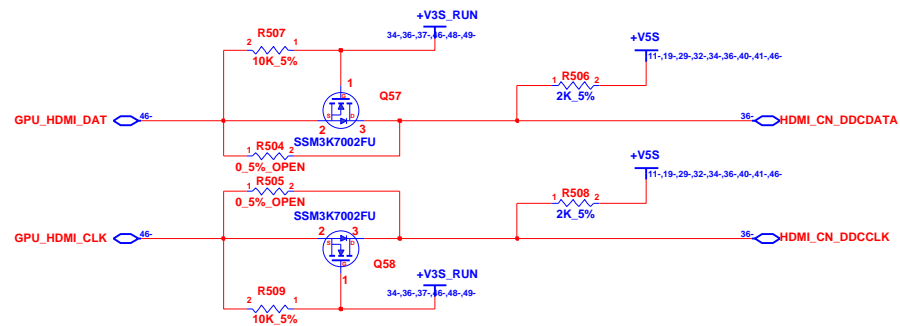
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	DGPU_HPD_INTR#
Plug Cable	L
Unplug Cable	H



	GPU_HDMI_HPD
Plug Cable	H
Unplug Cable	L

	HDMI_HPD_CN
Plug Cable	H
Unplug Cable	L

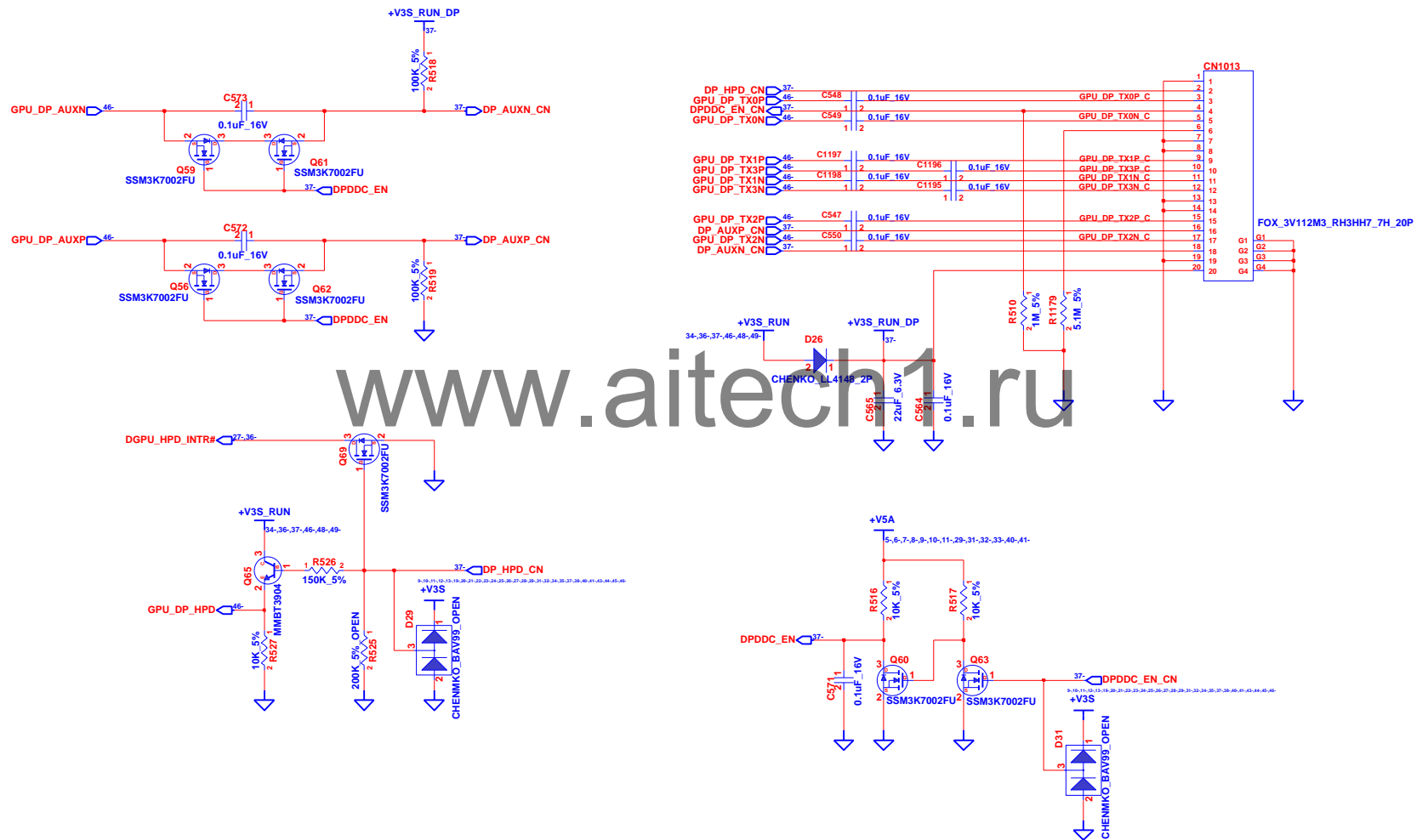


**INVENTEC**

TITLE			
Romeo			
HDMI			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2
SHEET		36	OF 65

CHANGE by: Evan Shih 12-Nov-2009

# Mini Display Port

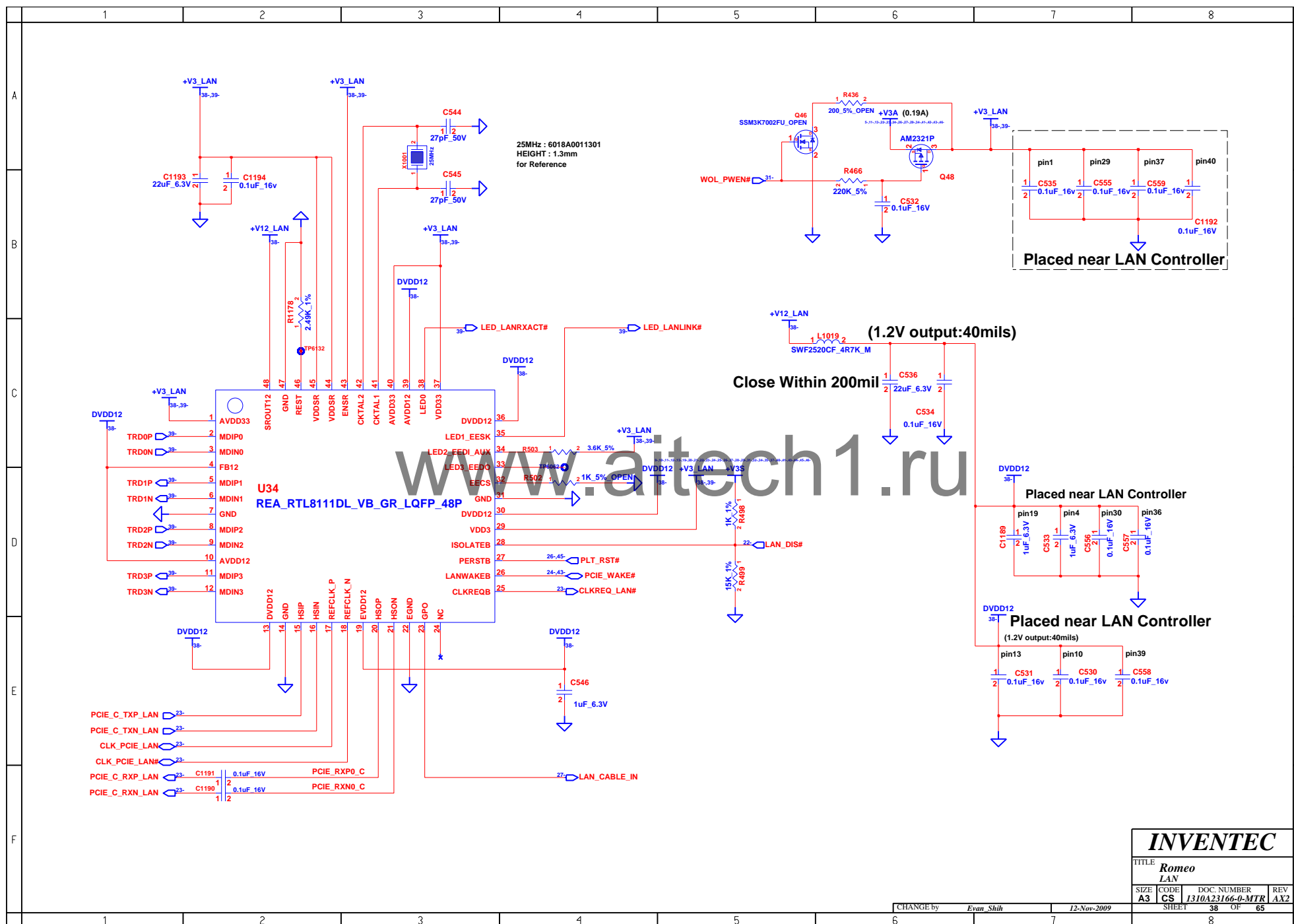


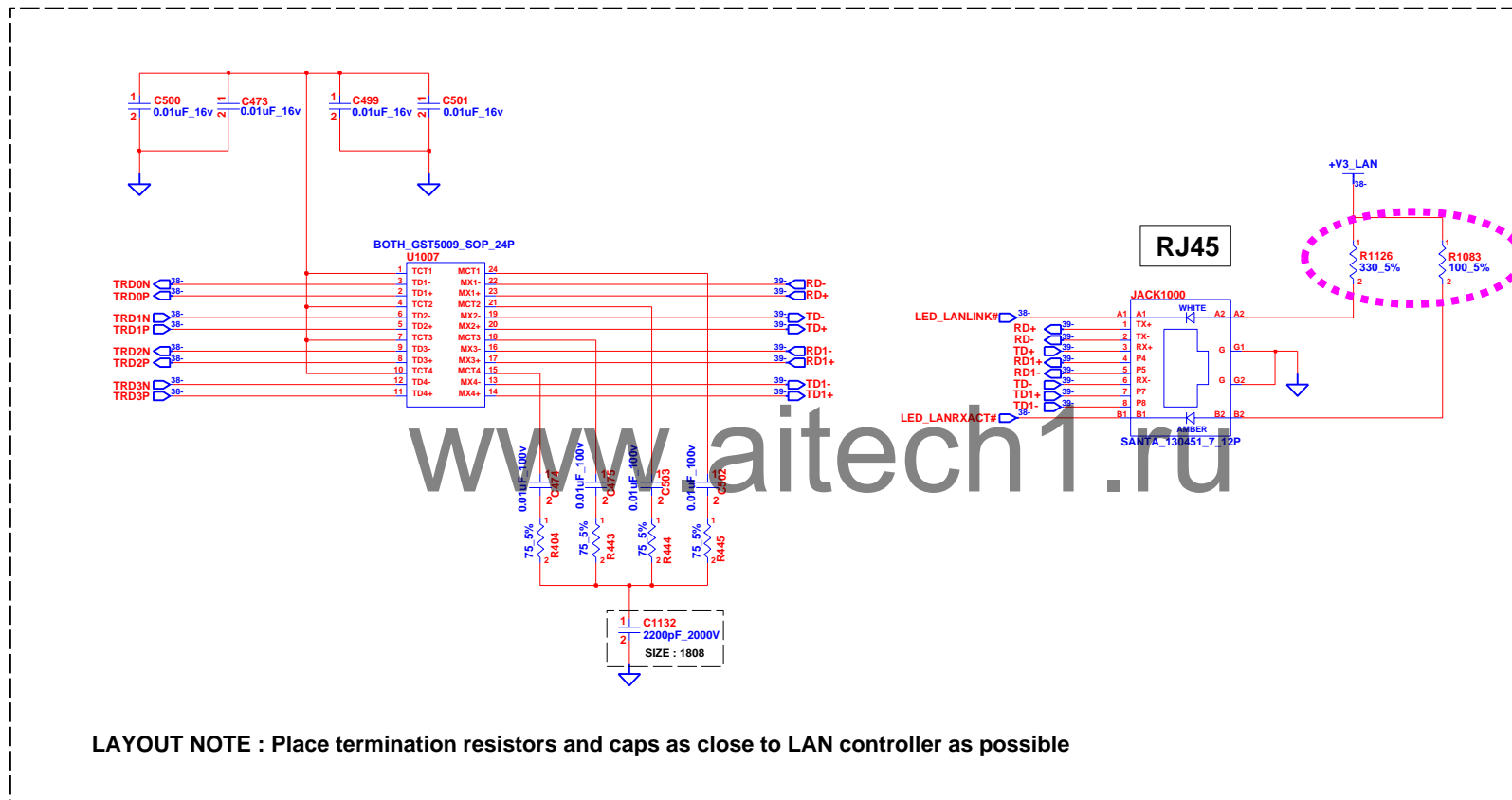
www.aitech1.ru

**INVENTEC**

TITLE			
Romeo DisplayPort			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2
SHEET		37	OF 65

CHANGE by Evan Shih 12-Nov-2009





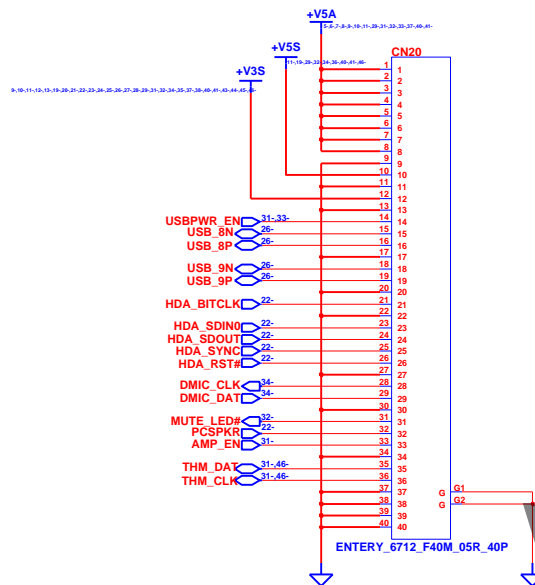
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SHEET		39	OF 65

CHANGE by *Evan Shih* 12-Nov-2009

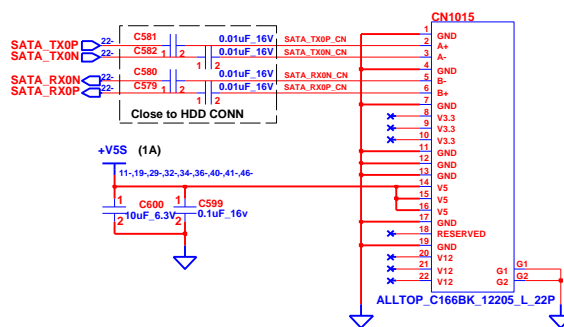




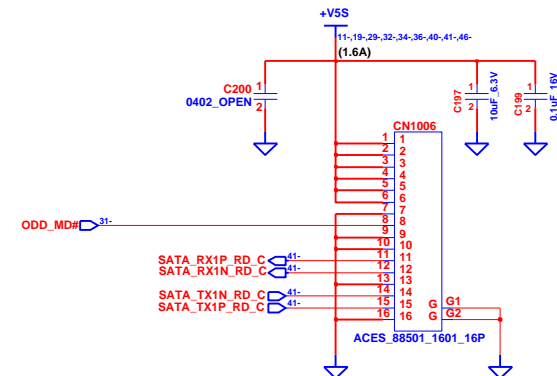
## AU Board CN on MB



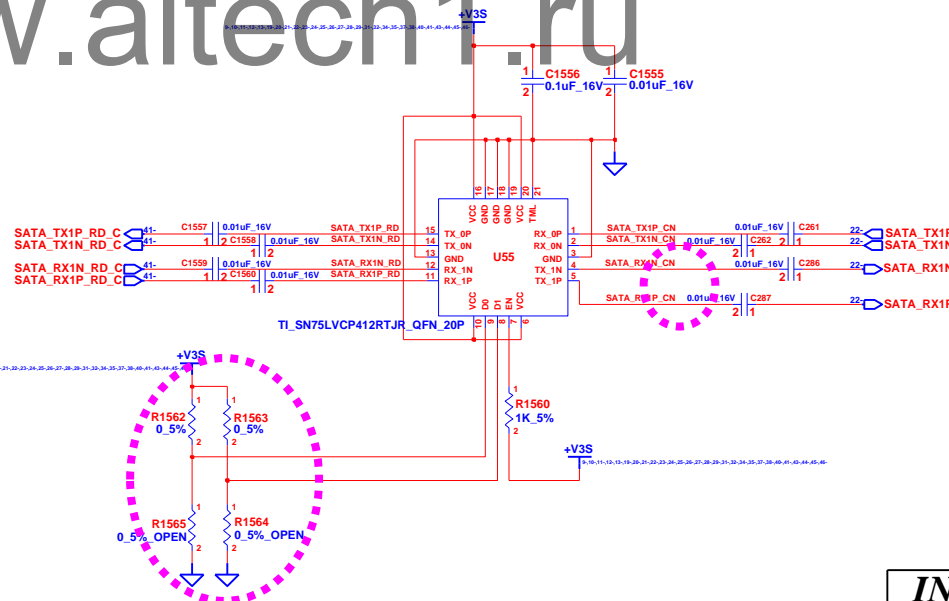
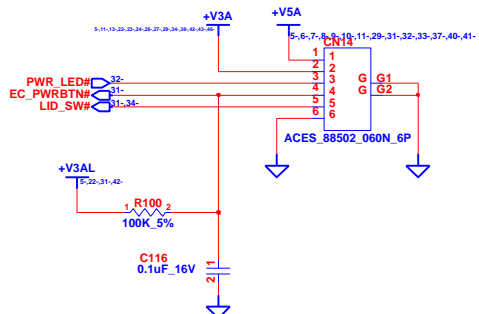
## SATA HDD CONN



## SATA ODD FFC CONN on MB



## PBN Cable CN on MB



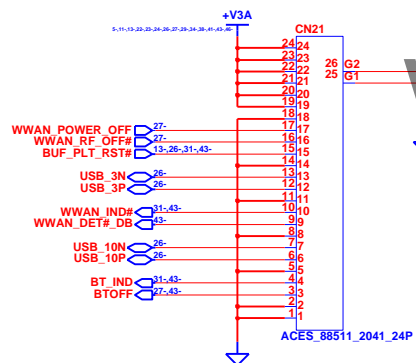
**INVENTEC**

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SHEET <b>41</b>		OF <b>65</b>	

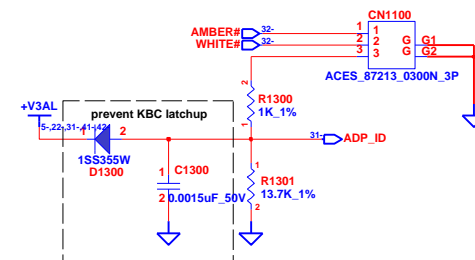
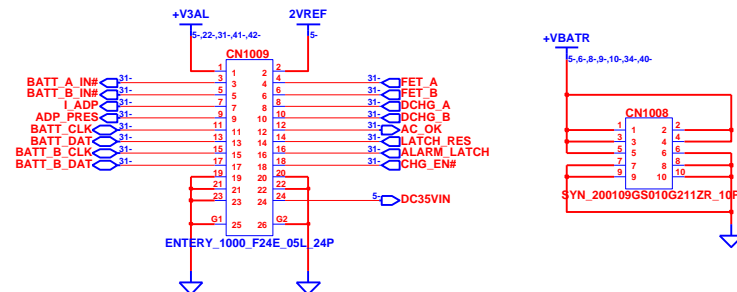
CHANGE by *Evan Shih* 12-Nov-2009

## WC Board CN on MB

### WWAN & BT



## CHG Board CN on MB



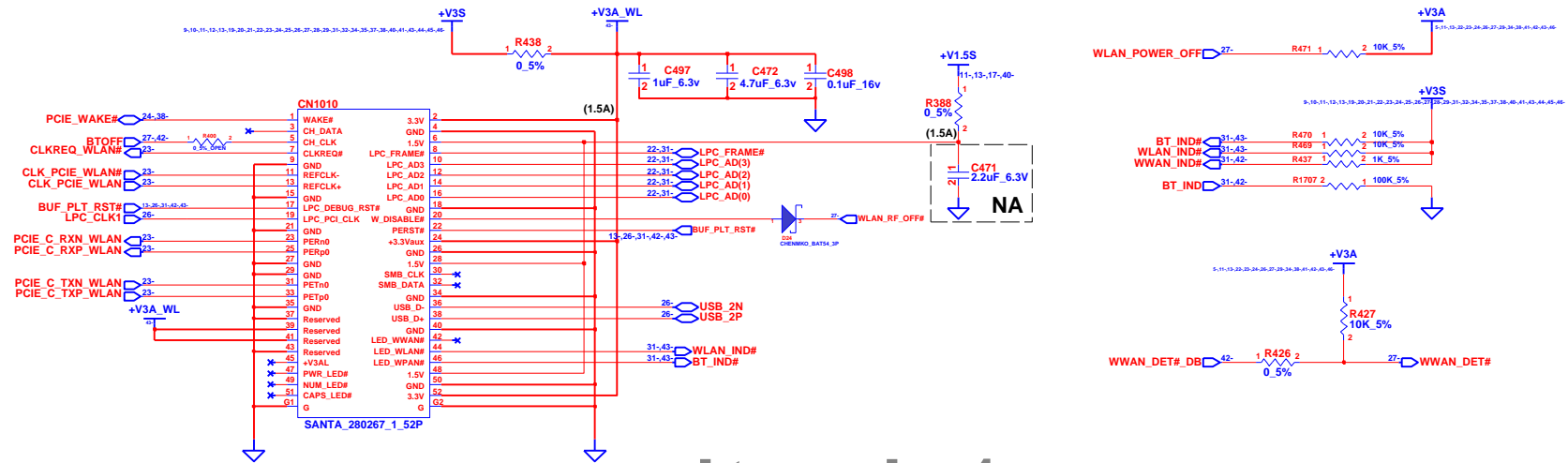
**INVENTEC**

TITLE			
Romeo Connector-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2
SHEET		42	OF 65

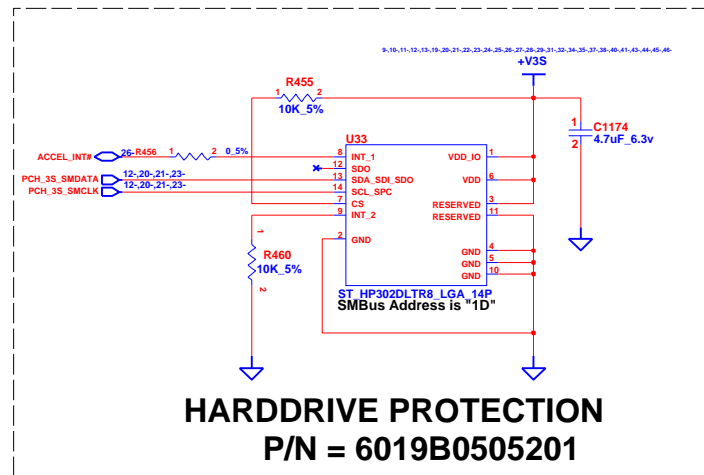
CHANGE by Evan Shih 12-Nov-2009

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# WLAN CONN (MINICARD)



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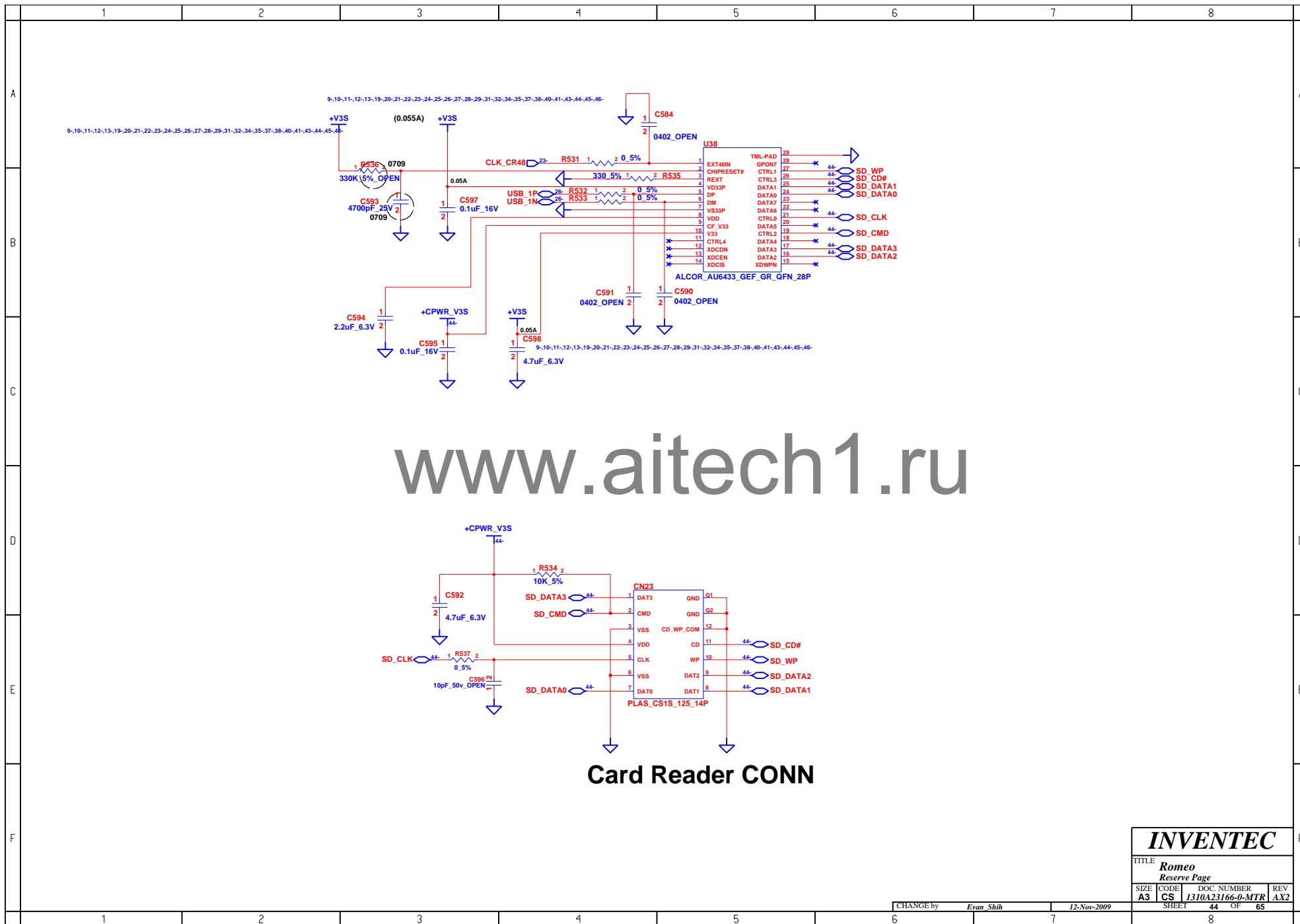


**HARDDRIVE PROTECTION**  
P/N = 6019B0505201

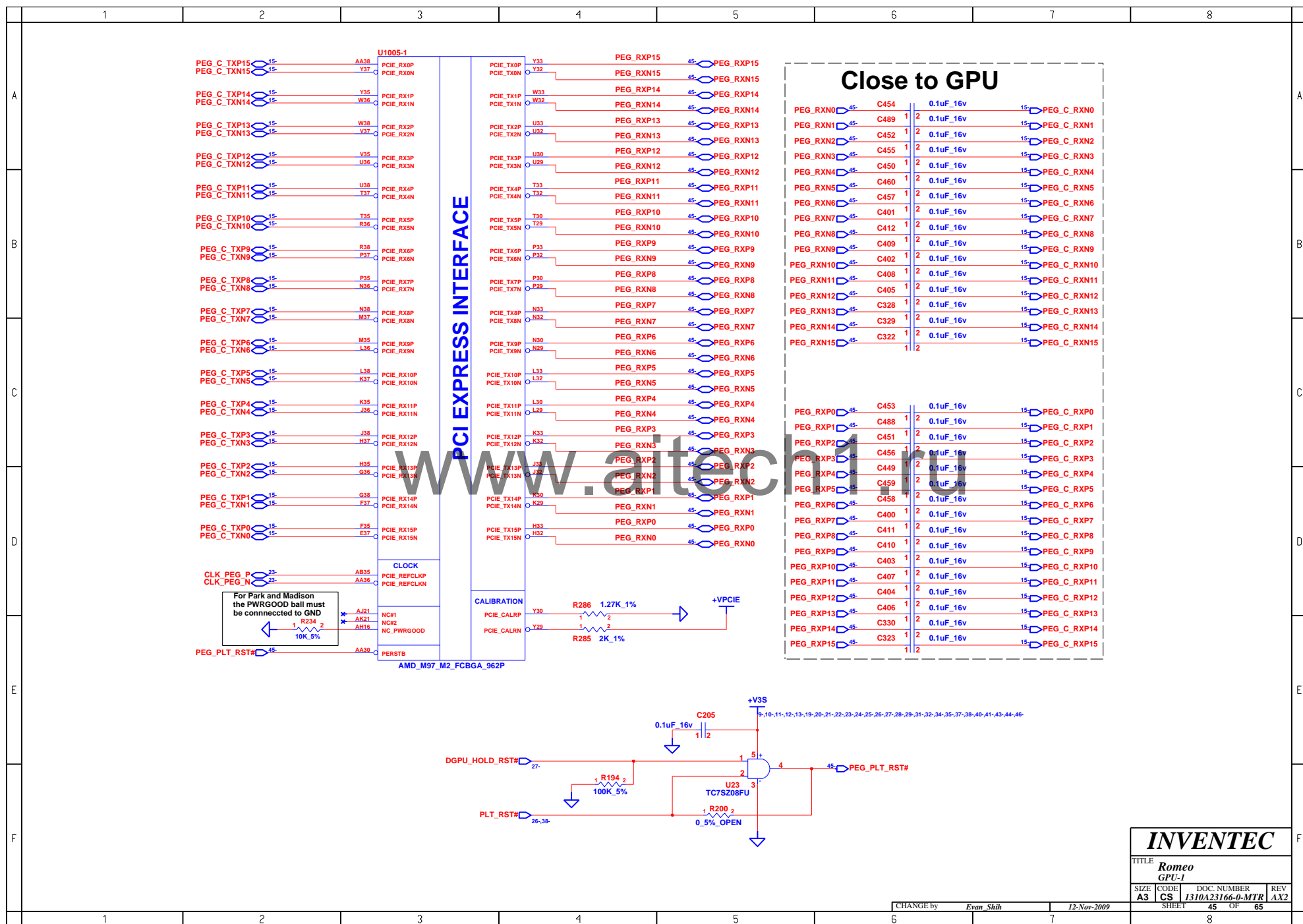
**INVENTEC**

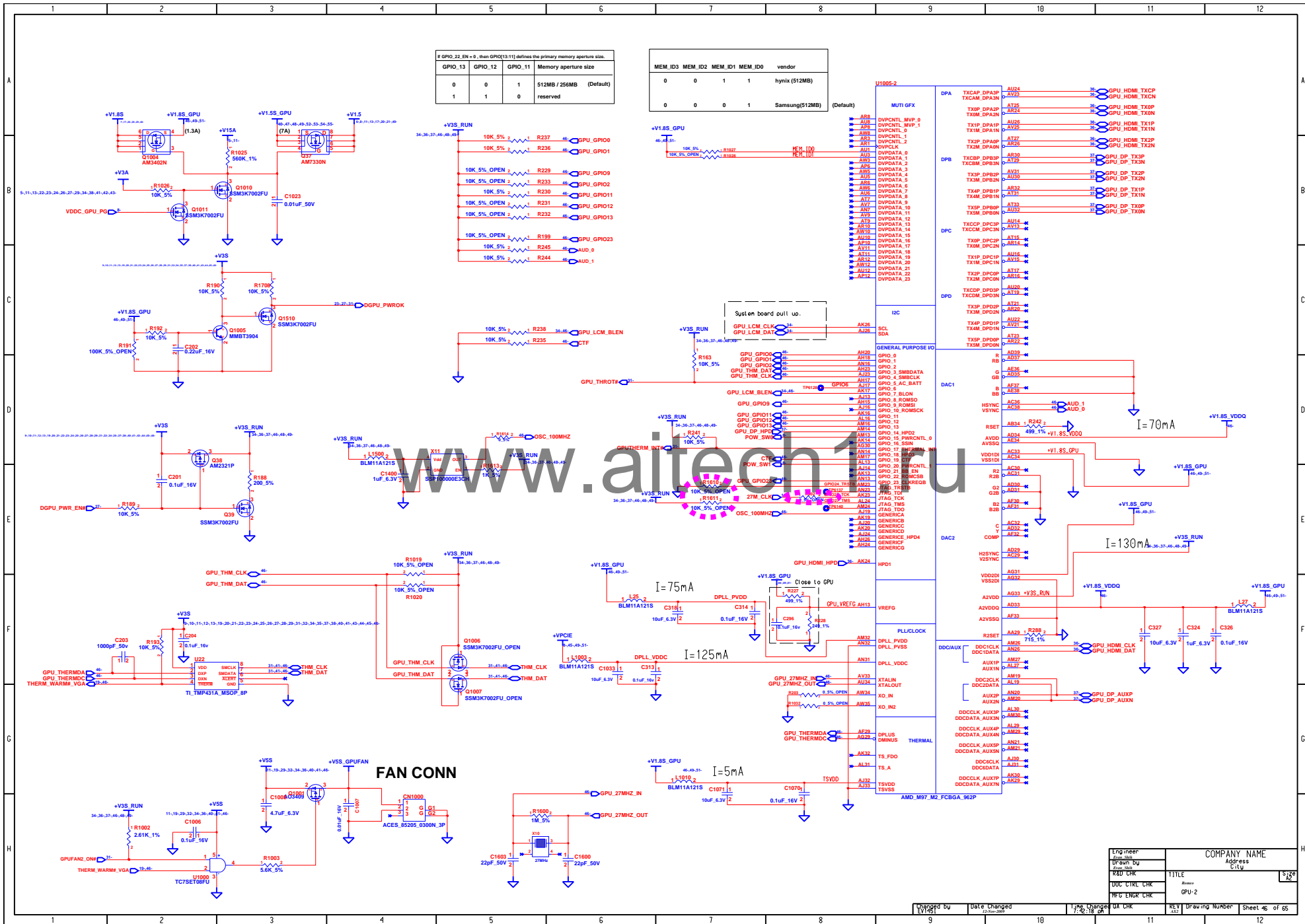
TITLE <b>Romeo</b> WLAN & HDD Lock			
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	REV <b>AX2</b>
SHEET <b>43</b>		OF <b>65</b>	

CHANGE by *Evan Shih* 12-Nov-2009



INVENTEC			
TITLE Romeo Reserve Page			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV AX2
CHANGE by Evan Shih		12-Nov-2009	
SHEET		44 OF 65	



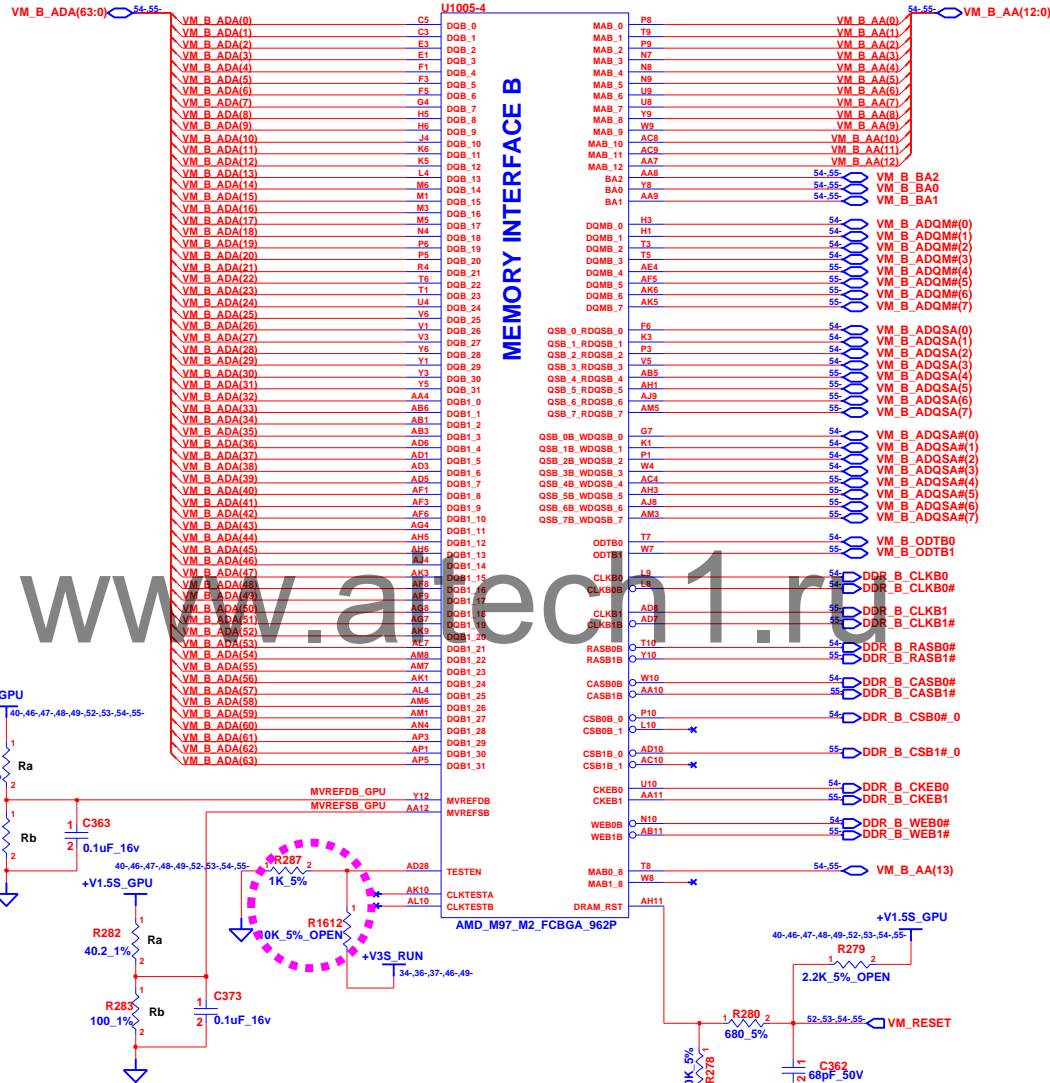




# Channel B

DDR3/GDDR3Memory Stuff Option

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R

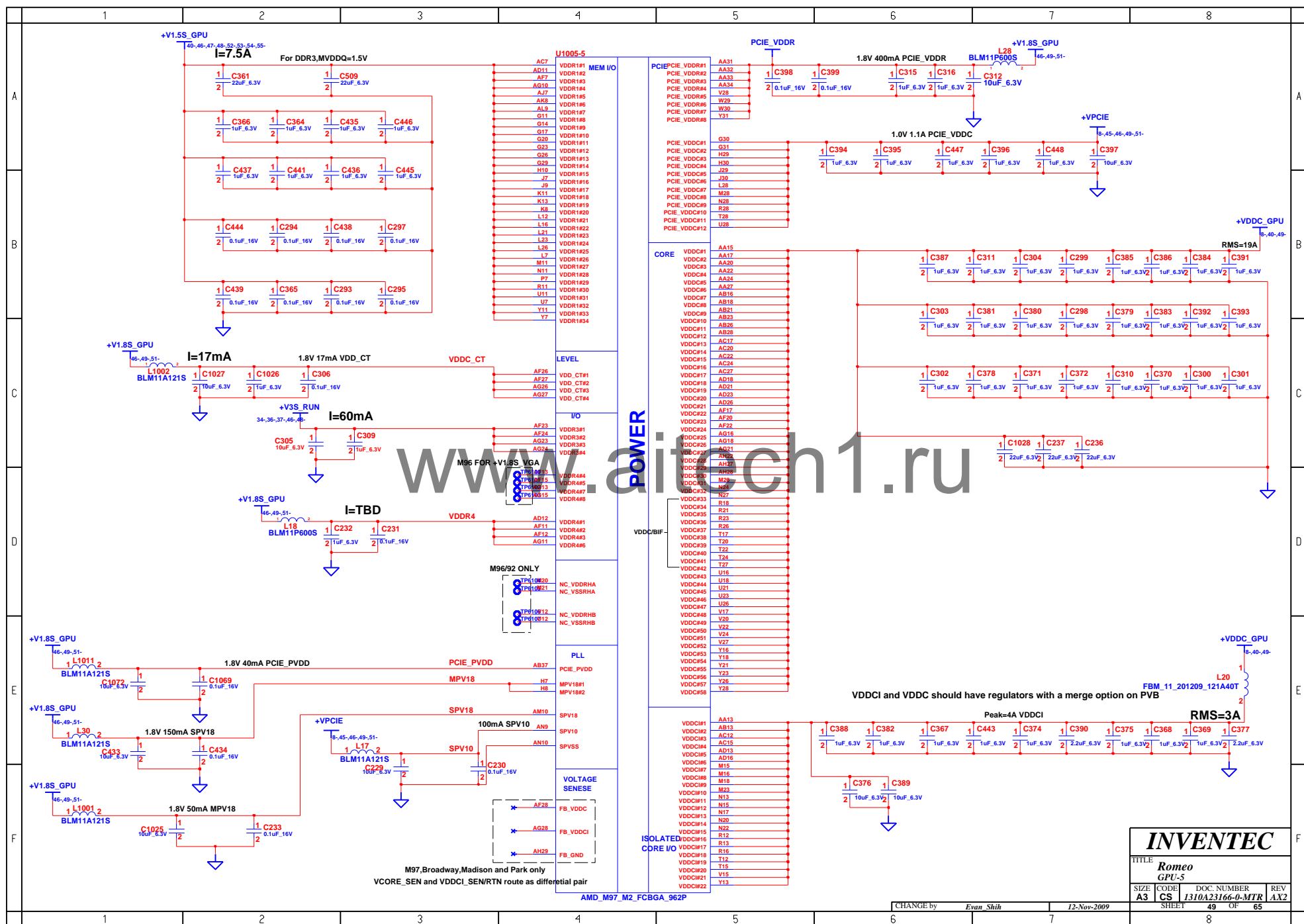


INVENTEC

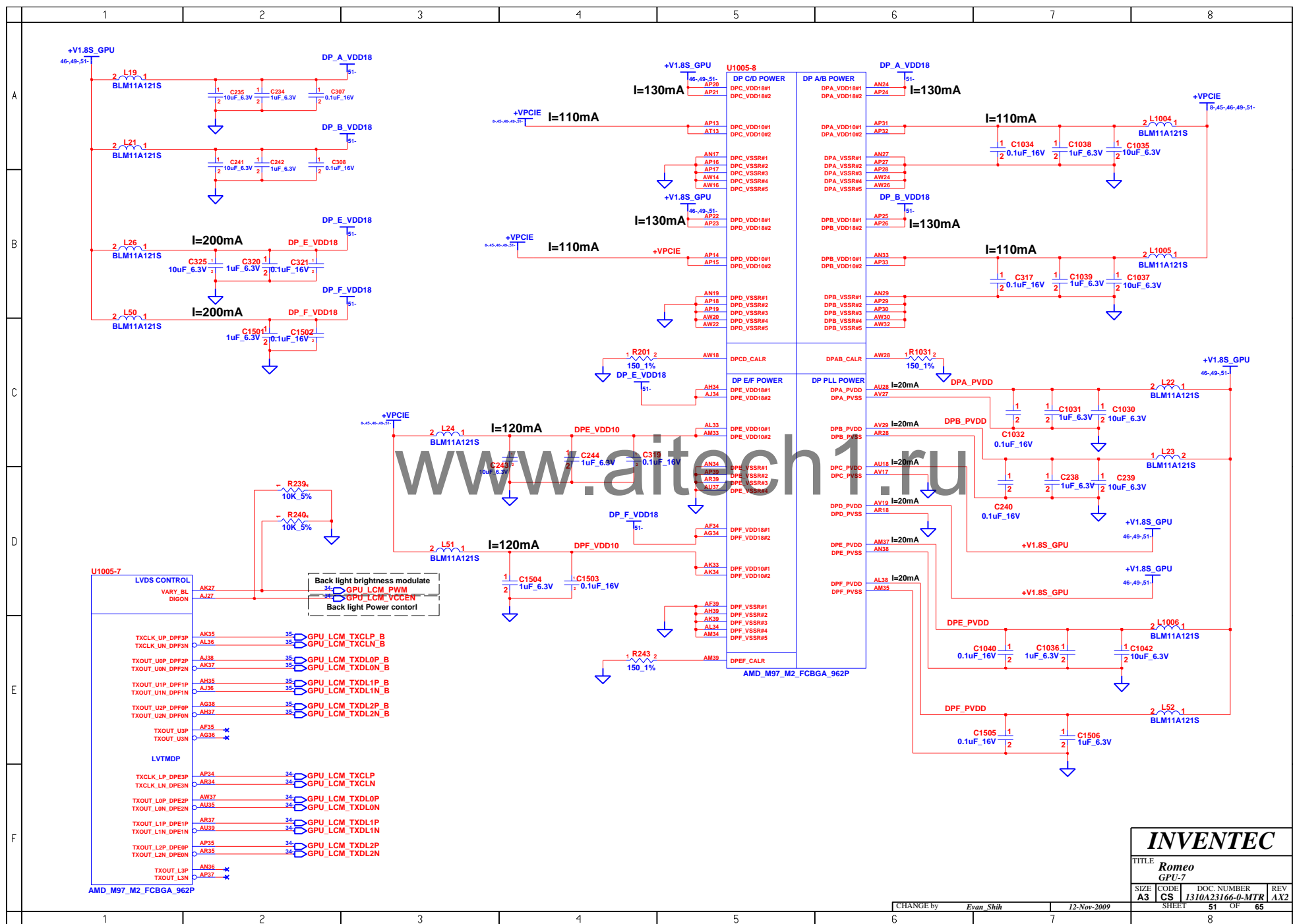
TITLE Romeo GPU-4			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV AX2
SHEET 48		OF 65	

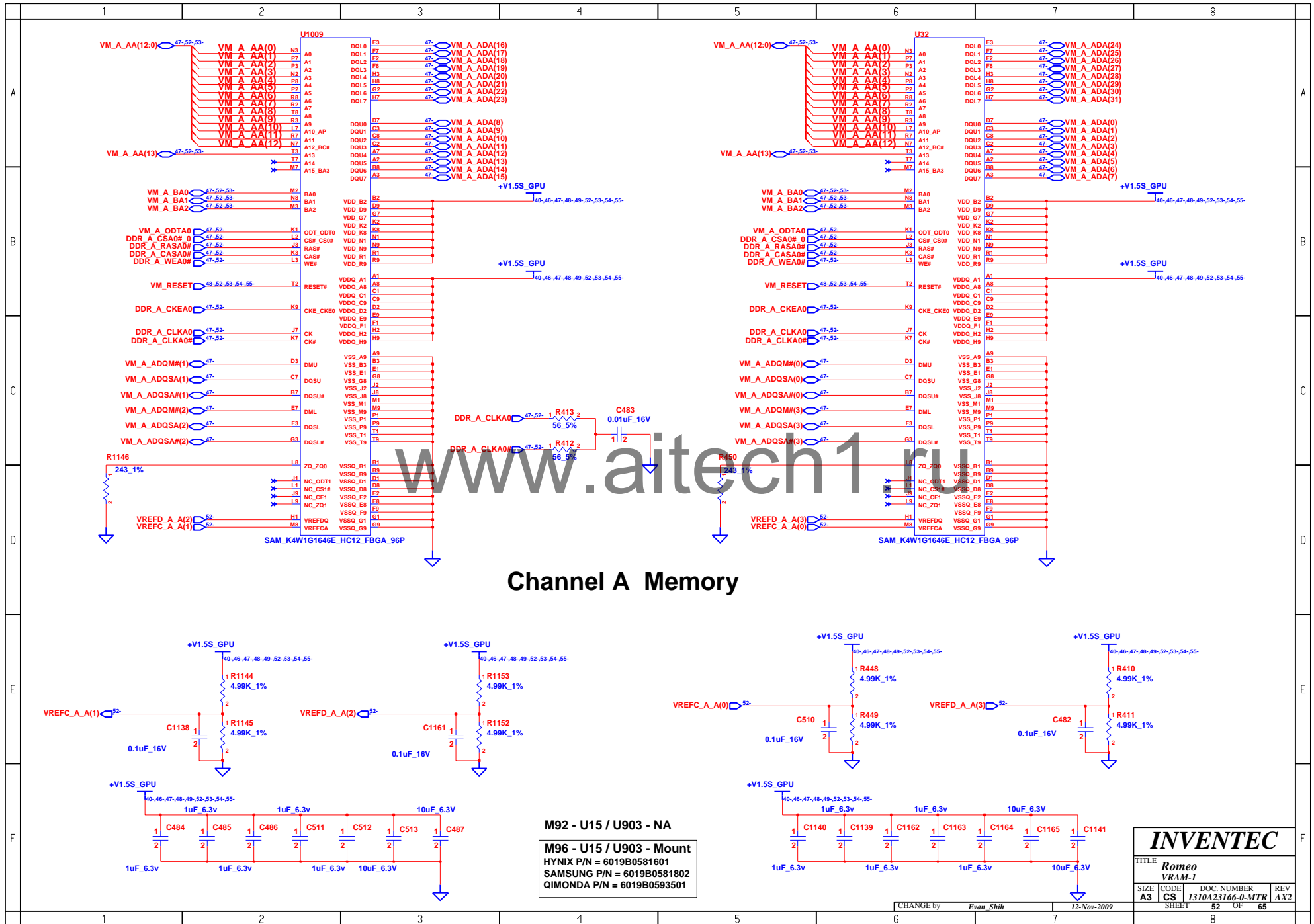
CHANGE by Evan Shih 12-Nov-2009





[illegible]





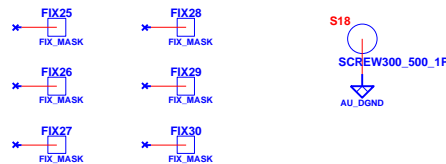
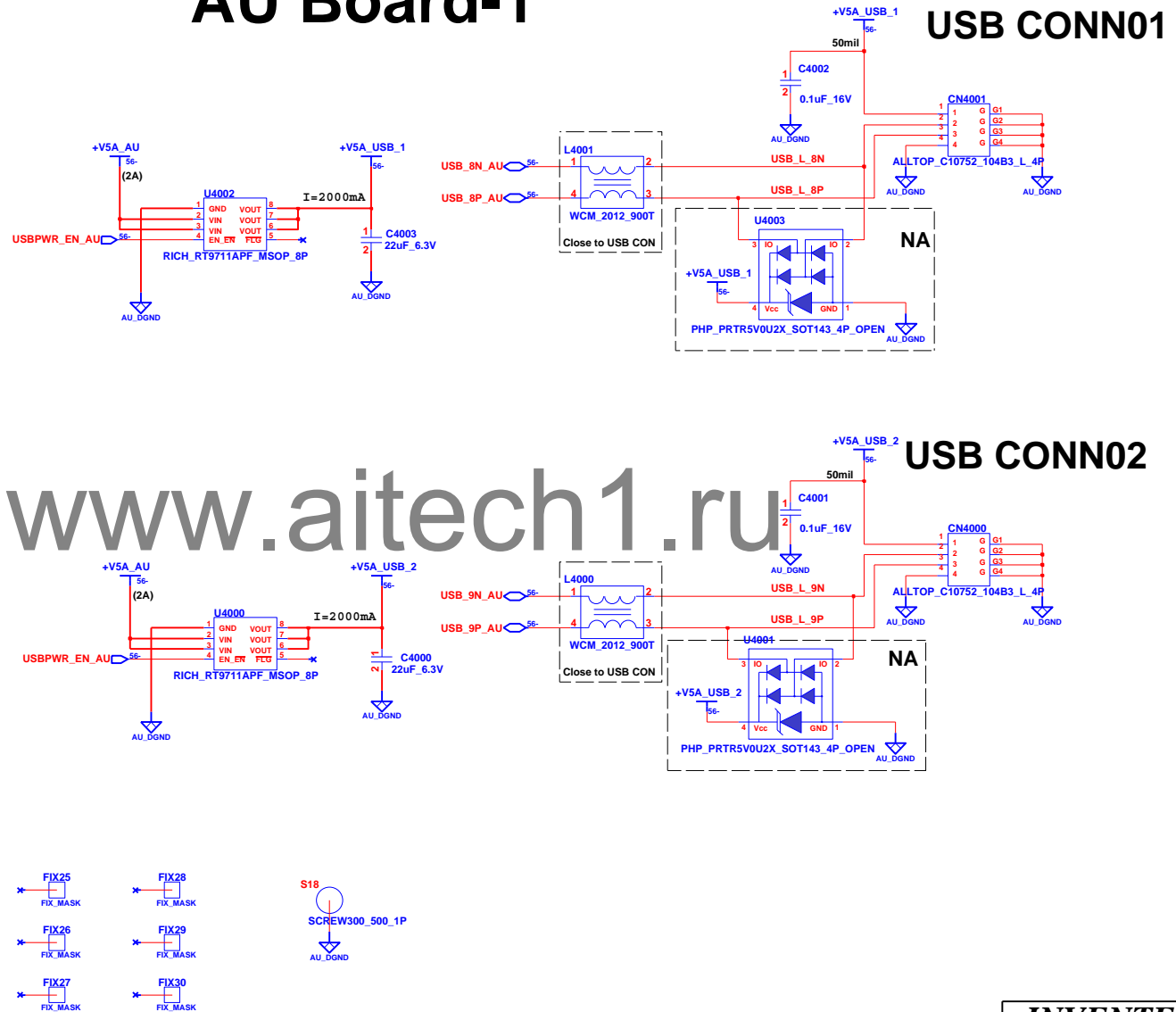
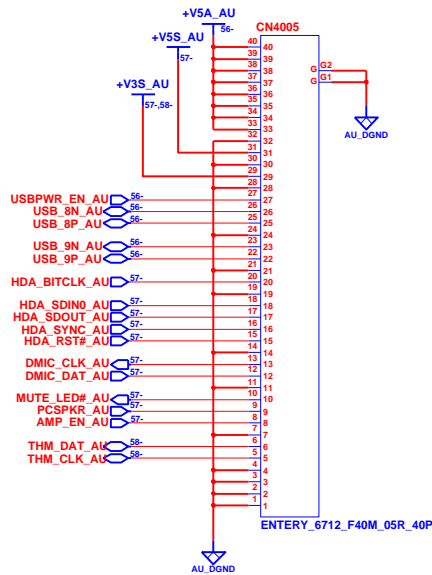






# AU Board-1

## AU Board CN on DB



**INVENTEC**

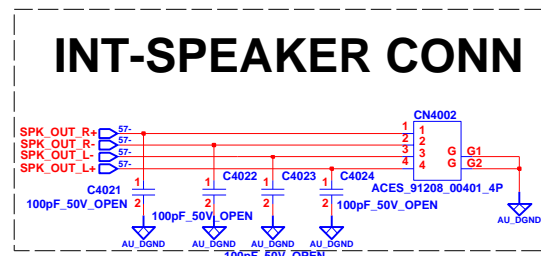
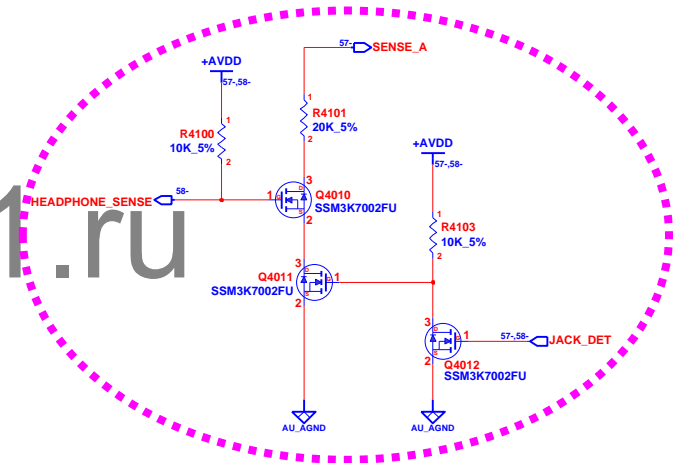
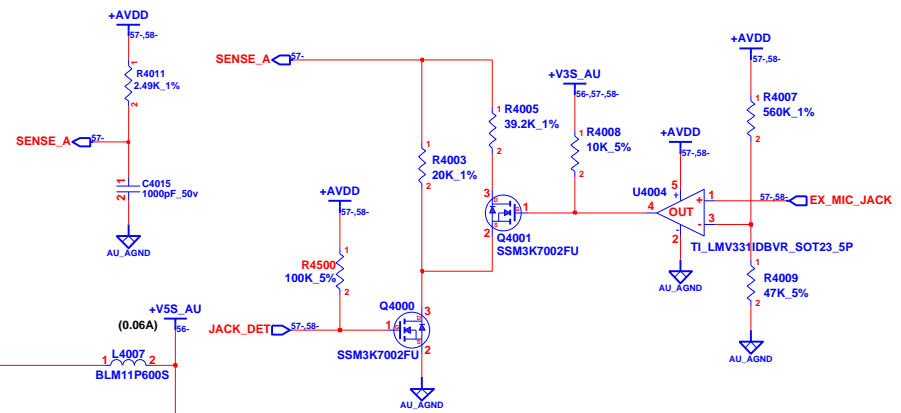
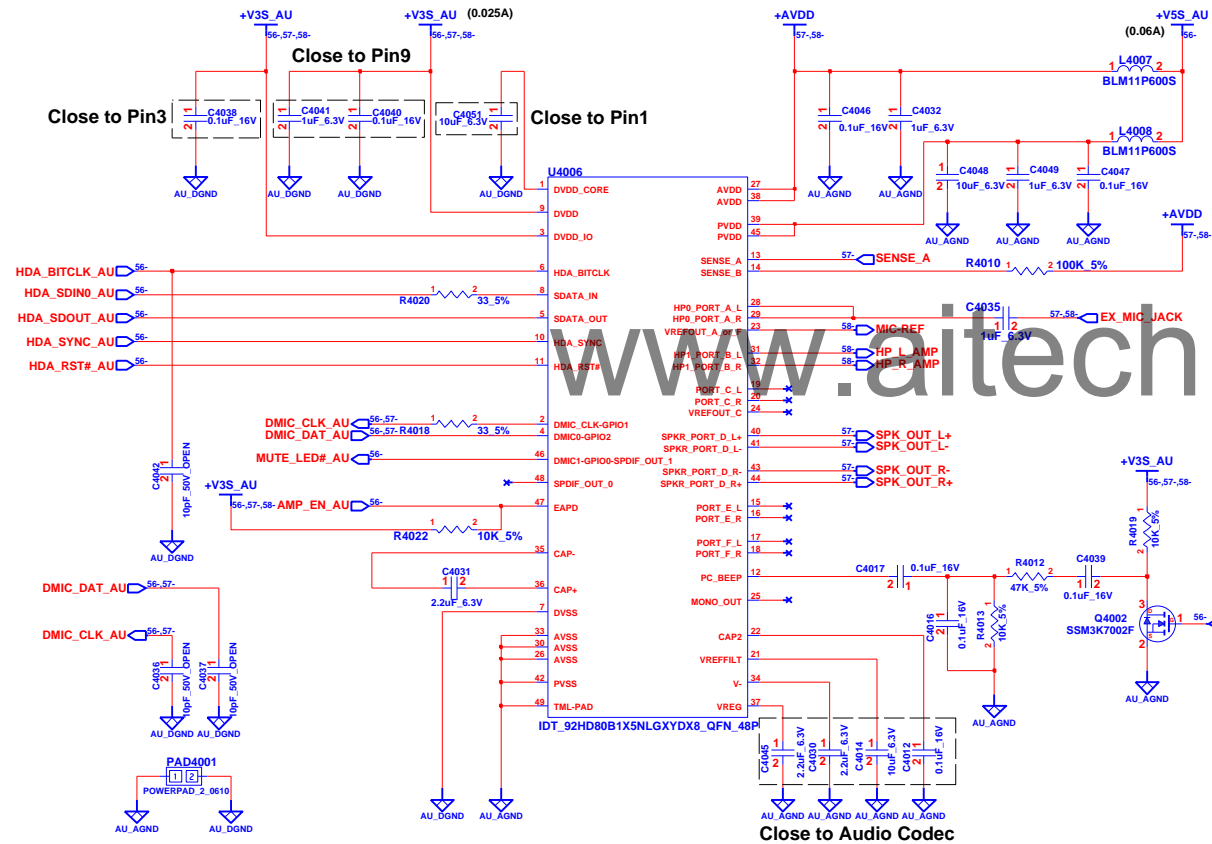
TITLE			
Romeo			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2
SHEET		56	OF 65

CHANGE by: Evan Shih 12-Nov-2009



# AU Board-2

PORT	Discription	Sense
A	Microphone	SENSE_A
B	Headphone	SENSE_A
C	NO USE	
D	SPKR	
E	NO USE	
F	NO USE	



**INVENTEC**

TITLE: **Romeo**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2

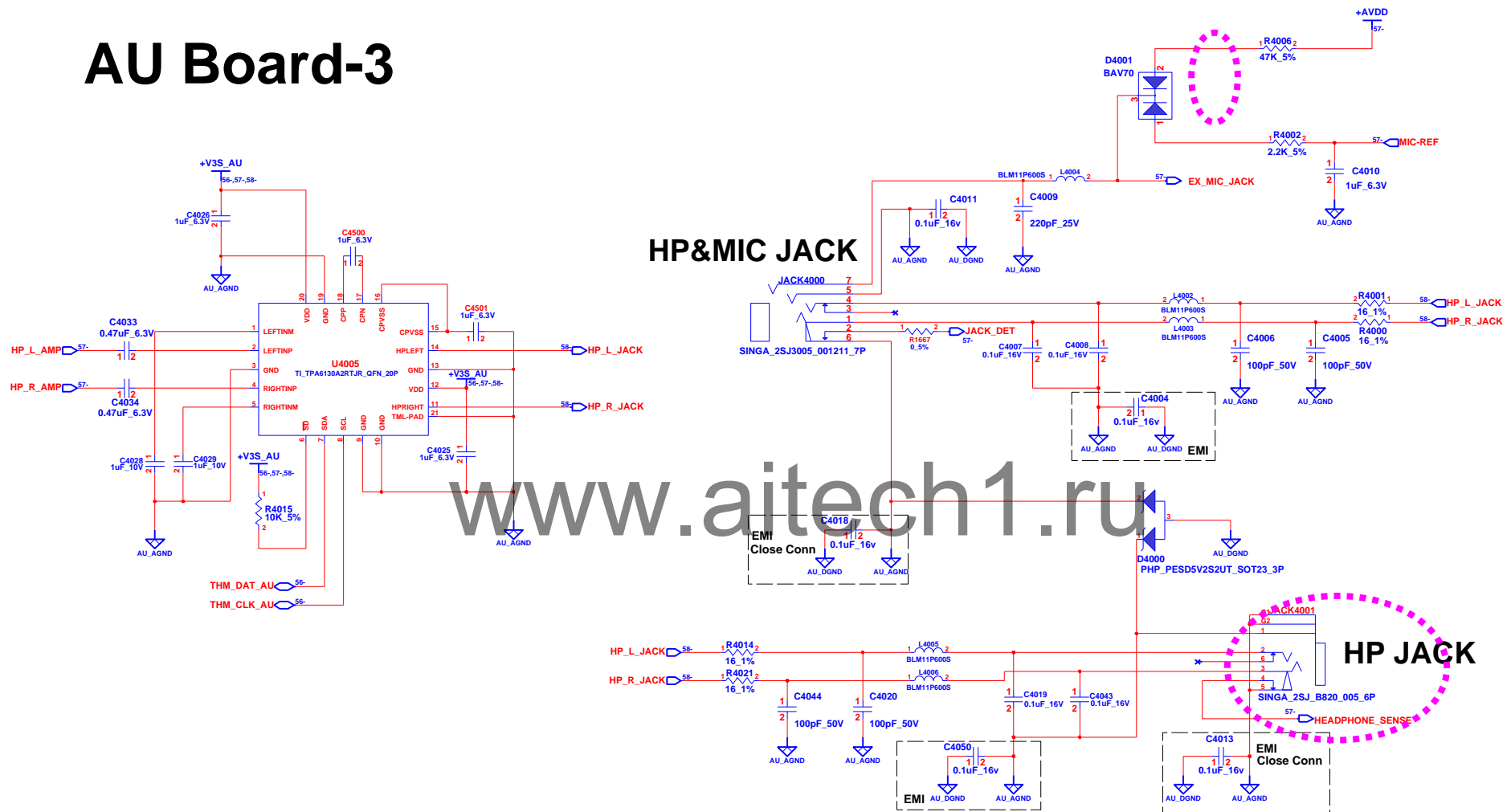
CHANGE by: **Evan Shih** 12-Nov-2009

SHEET 57 OF 65

# AU Board-3

## HP&MIC JACK

## HP JACK



**INVENTEC**

TITLE  
**Romeo**

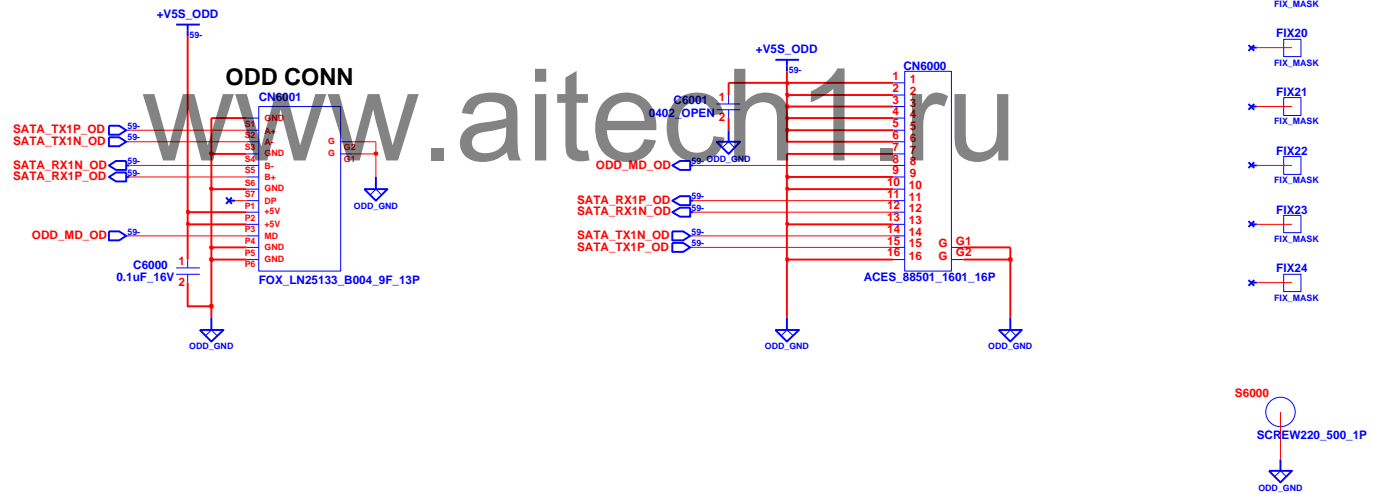
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2

CHANGE by *Evan Shih* 12-Nov-2009

SHEET 58 OF 65

# SATA ODD

## ODD Board



- FIX19
- FIX20
- FIX21
- FIX22
- FIX23
- FIX24

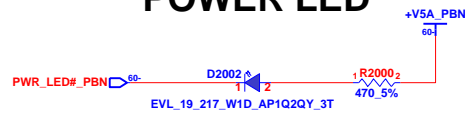
S6000  
SCREW220\_500\_1P

INVENTEC			
TITLE			
Romeo			
ODD EXT BOARD			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2
SHEET		59	OF 65

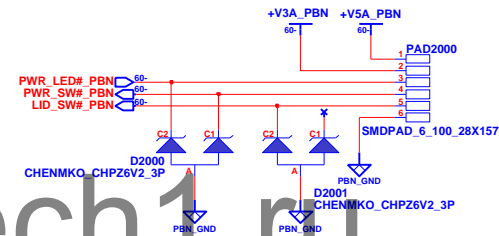
CHANGE by Evan Shih 12-Nov-2009

# Power Button

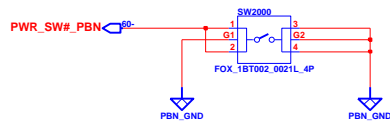
## POWER LED



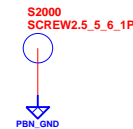
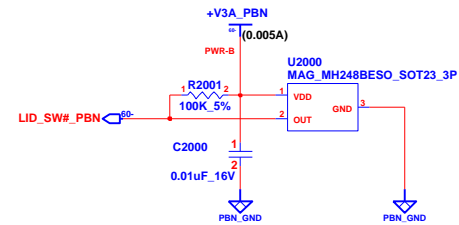
## PBN Cable CN on PBN



## POWER BUTTON

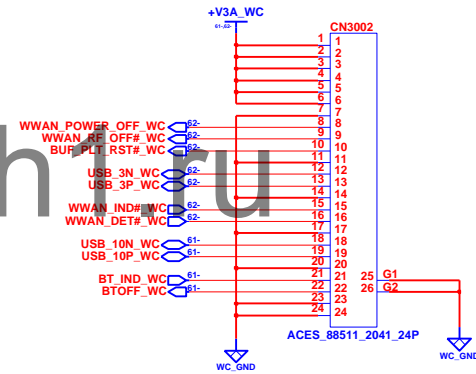
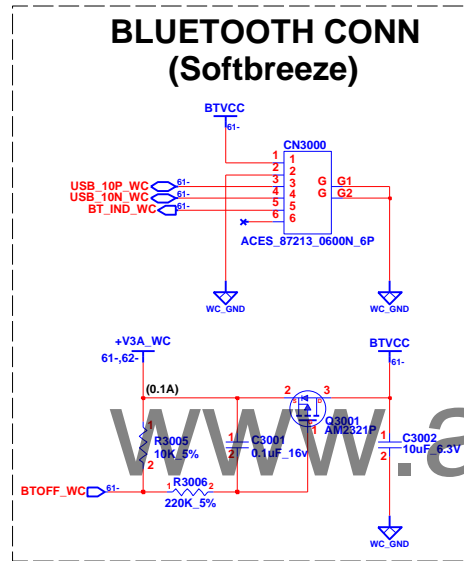
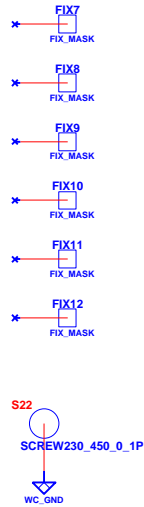


## LID SWITCH

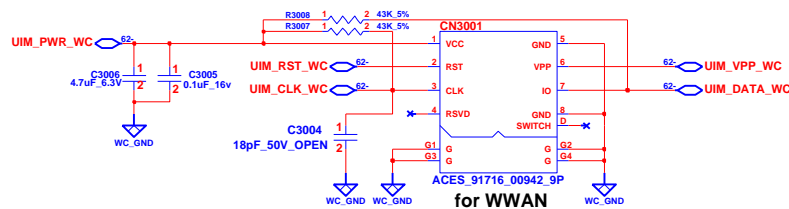


INVENTEC			
TITLE Romeo			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV AX2
CHANGE by Evan Shih		12-Nov-2009	
SHEET 60		OF 65	

# WC Board-1



## SIM CARD CONN



**INVENTEC**

TITLE  
**Romeo**

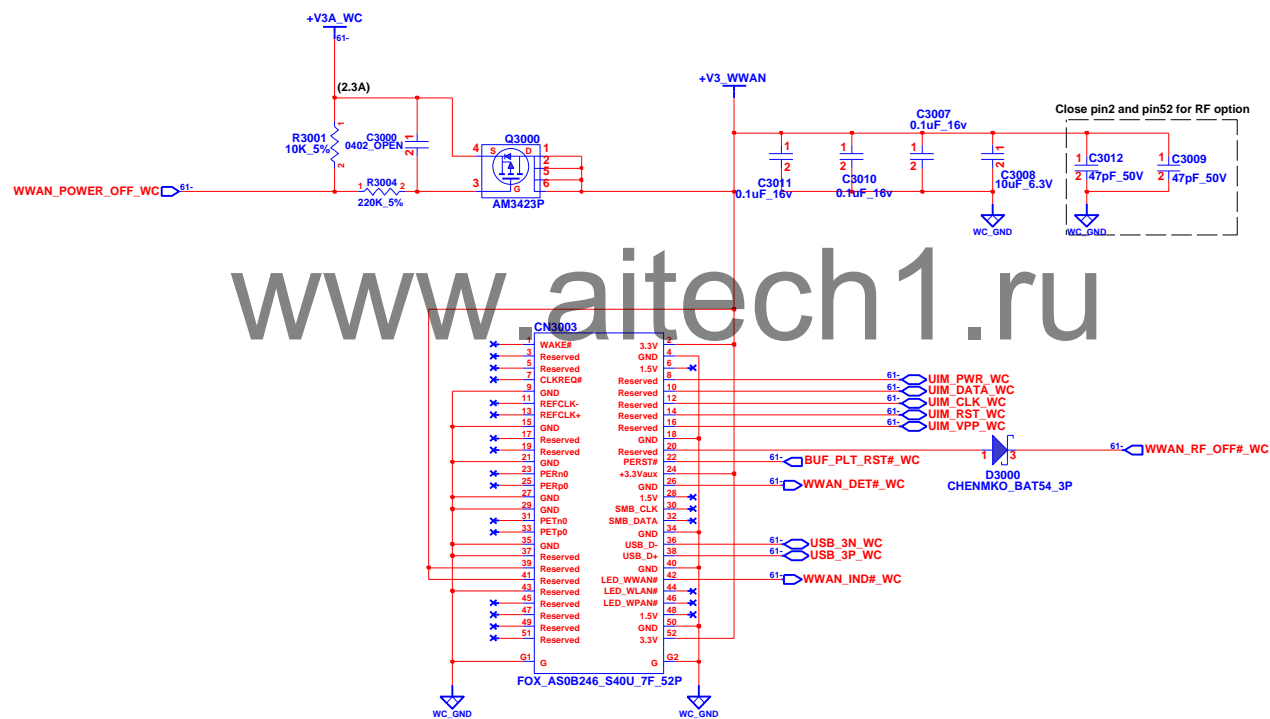
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2

CHANGE by *Evan Shih* 12-Nov-2009

SHEET 61 OF 65

# WC Board-2

## WWAN



**INVENTEC**

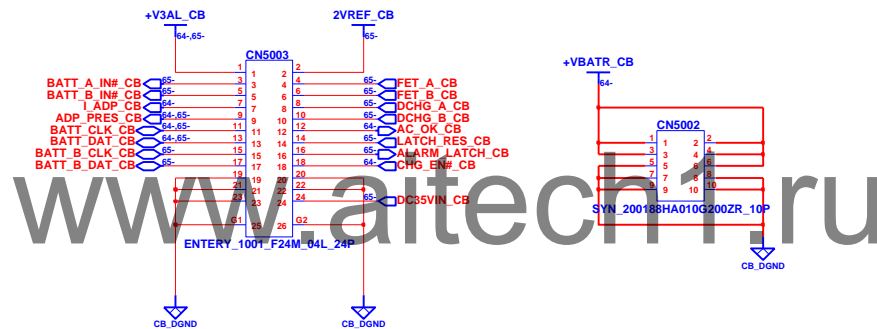
TITLE  
**Romeo**

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A23166-0-MTR	AX2

CHANGE by *Evan Shih* 12-Nov-2009

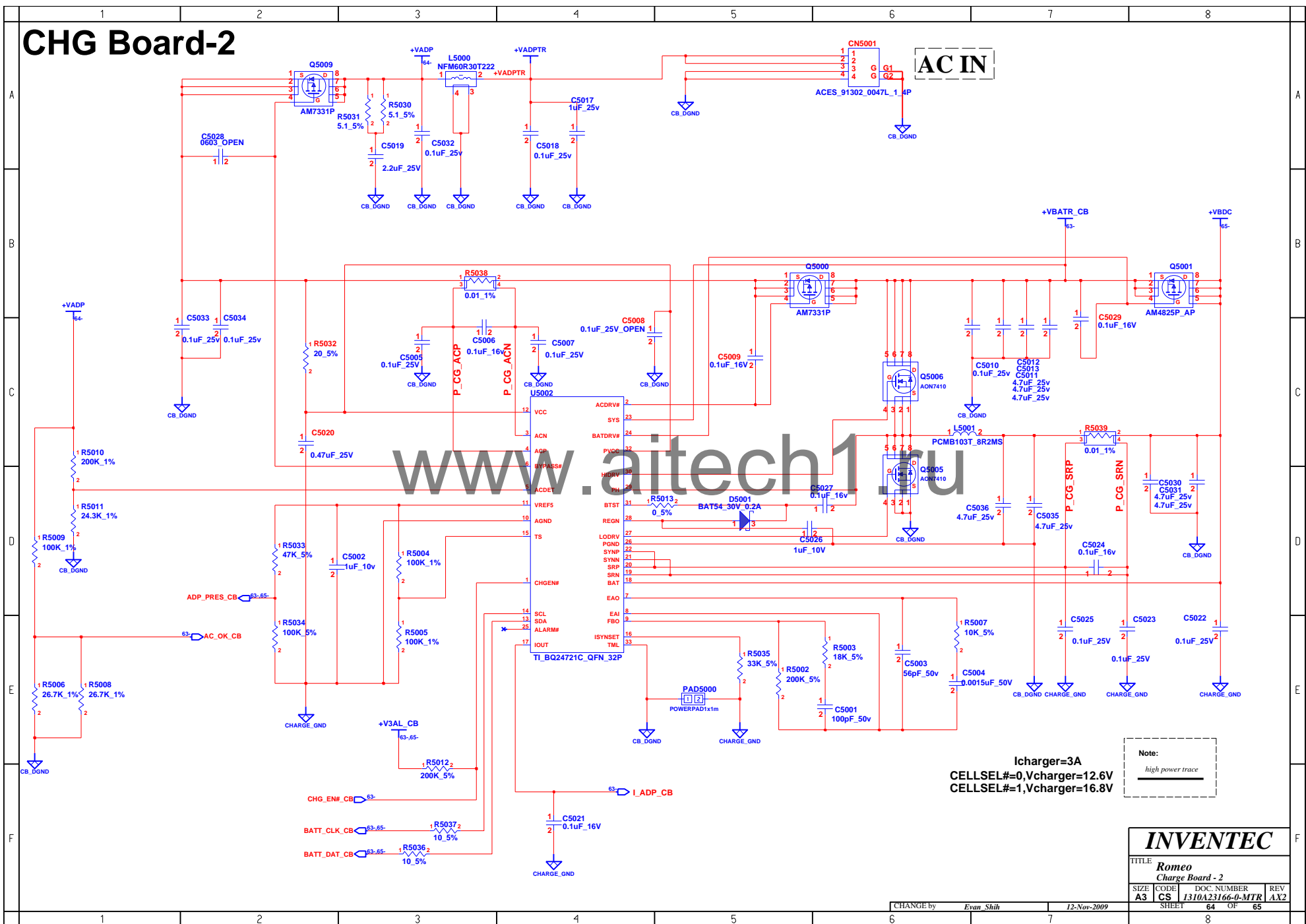
SHEET 62 OF 65

# CHG Board-1



INVENTEC			
TITLE Romeo Charge Board - 1			
SIZE A3	CODE CS	DOC. NUMBER 1310A23166-0-MTR	REV AX2
CHANGE by Evan Shih		12-Nov-2009	
SHEET		63 OF 65	

# CHG Board-2



**Note:**  
*high power trace*

**Icharger=3A**  
**CELLSEL#=0,Vcharger=12.6V**  
**CELLSEL#=1,Vcharger=16.8V**

**INVENTEC**

TITLE	<b><i>Romeo</i></b> <b><i>Charge Board - 2</i></b>
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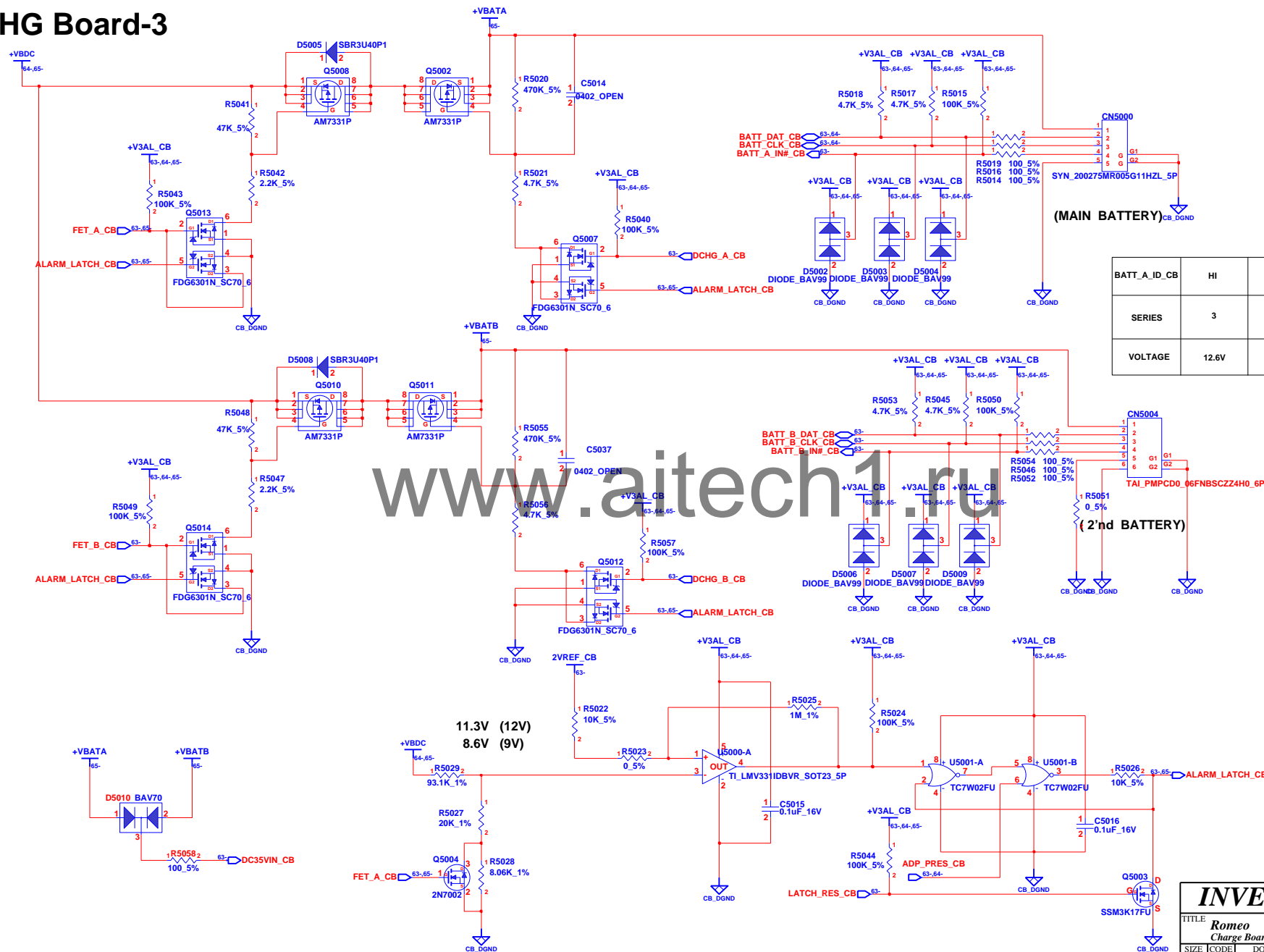
SIZE <b>A3</b>	CODE <b>CS</b>	DOC. NUMBER <b>1310A23166-0-MTR</b>	R <b>A</b>
SHEET		64	OF 65

CHANGE by	<i>Evan Shih</i>	<i>12-Nov-2009</i>
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# CHG Board-3



BATT_A_ID_CB	HI	LOW
SERIES	3	4
VOLTAGE	12.6V	16.8V

**INVENTEC**

TITLE: **Romeo Charge Board - 3**

SIZE: **A3** CODE: **CS** DOC. NUMBER: **1310A23166-0-MTR** REV: **AX2**

SHEET: **65** OF: **65**

CHANGE by: **Evan Shih** 12-Nov-2009